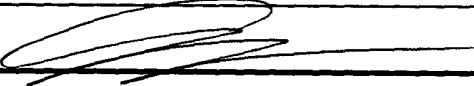


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Title MULTI-DOMAIN LIQUID CRYSTAL DISPLAY DEVICE

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APPLICATION ELEMENTS <i>See MPEP chapter 600 concerning utility patent application contents</i>	ADDRESS TO: Assistant Commissioner for Patents Box Patent Application Washington, DC 20231
<p>1. <input checked="" type="checkbox"/> Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)</p> <p>2. <input checked="" type="checkbox"/> Specification Total Pages 28</p> <p>3. <input checked="" type="checkbox"/> Drawing(s) (35 U.S.C. 113) Total Sheets 41</p> <p>4. <input checked="" type="checkbox"/> Oath or Declaration Total Pages 1</p> <p>a. <input checked="" type="checkbox"/> Newly executed (original)</p> <p>b. <input type="checkbox"/> Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 15 completed)</p> <p>i. <input type="checkbox"/> DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b)</p> <p><input type="checkbox"/> Incorporation By Reference (usable if box 4B is checked) The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4B, is considered to be part of the disclosure of the accompanying application and is hereby incorporated by reference therein</p>	<p>ACCOMPANYING APPLICATION PARTS</p> <p>6. <input checked="" type="checkbox"/> Assignment Papers (cover sheet & document(s))</p> <p>7. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement <input type="checkbox"/> Power of Attorney (when there is an assignee)</p> <p>8. <input type="checkbox"/> English Translation Document (if applicable)</p> <p>9. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations</p> <p>10. <input type="checkbox"/> Preliminary Amendment</p> <p>11. <input checked="" type="checkbox"/> White Advance Serial No. Postcard</p> <p>12. <input type="checkbox"/> Small Entity Statement(s) <input type="checkbox"/> Statement filed in prior application. Status still proper and desired.</p> <p>13. <input type="checkbox"/> Certified Copy of Priority Document(s) (if foreign priority is claimed)</p> <p>14. <input type="checkbox"/> Other:</p>
<p>15. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below:</p> <p><input type="checkbox"/> Continuation <input type="checkbox"/> Divisional <input type="checkbox"/> Continuation-in-part (CIP) of prior application no.: Prior application information: Examiner: Group Art Unit:</p>	
<p>16. Amend the specification by inserting before the first line the sentence:</p> <p>This application is a <input type="checkbox"/> Continuation <input type="checkbox"/> Division <input type="checkbox"/> Continuation-in-part (CIP) of application Serial No. Filed on</p> <p><input type="checkbox"/> This application claims priority of provisional application Serial No. Filed</p>	
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MULTI-DOMAIN LIQUID CRYSTAL DISPLAY DEVICE

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to a liquid crystal display device, and more particularly, to a multi-domain liquid crystal display device in which a common auxiliary electrode is formed on a layer equal to a gate line to surround a pixel region and a dielectric structure is formed on a common electrode so that a viewing angle extends.

Discussion of the Related Art

Recently, a liquid crystal display device which drives a liquid crystal by an auxiliary electrode electrically insulated from a pixel electrode without aligning the liquid crystal has been suggested. Such a related art liquid crystal display device will be described with reference to Figs. 1a and 1b.

As shown in Fig. 1a, the related art liquid crystal display device includes a first substrate, a second substrate 33, a plurality of data lines and gate lines, a thin film transistor, a pixel electrode 13, a passivation film 37, and an auxiliary electrode 21. The data lines and gate lines are formed on the first substrate lengthwise and crosswise to divide the first substrate into a plurality of pixel regions. The thin film transistor is formed in each pixel region on the first substrate

and includes a gate electrode, a gate insulating film, a semiconductor layer, an ohmic contact layer, and a source/drain electrode. The pixel electrode 13 is formed on the gate insulating film and the passivation film 37 is formed on the pixel electrode 13 including the whole first substrate. The auxiliary electrode 21 is formed on the passivation film 37 to partially overlap the pixel electrode 13. At this time, the pixel electrode may be etched in a specific form to form an open region so that the pixel region is divided.

The related art liquid crystal display device further includes a light-shielding layer, a color filter layer formed on the light-shielding layer, a common electrode 17 formed on the color filter layer, and a liquid crystal layer formed between the first substrate and the second substrate. The light-shielding layer is formed on the second substrate 33 to shield light leaked from the gate line, the data line, and the thin film transistor. An open region 27 may be formed in the common electrode 17 to distort electric field applied to the liquid crystal layer.

The auxiliary electrode 21 formed around the pixel electrode 13 and the open region 27 of the common electrode 17 distort electric field applied to the liquid crystal layer so that liquid crystal molecules are variously driven within a unit pixel. This is intended that a dielectric energy by the distorted electric field places a liquid crystal director at a desired position.

However, the liquid crystal display device requires the open region 27 in the pixel electrode 13 or the common electrode 17 to obtain multi-domain effect. To this end, the process for patterning the electrodes is additionally required.

Furthermore, if the open region 27 is not formed or has a small width, distortion range of the electric field required to divide the domain is weak. Accordingly, there is a problem that the time when the liquid crystal director reaches a stable state relatively becomes longer.

SUMMARY OF THE INVENTION

Accordingly, the present invention is directed to a multi-domain liquid crystal display device that substantially obviates one or more of the problems due to limitations and disadvantages of the related art.

An object of the present invention is to provide a multi-domain liquid crystal display device in which a common auxiliary electrode is formed on a layer equal to a gate line to surround a pixel region and a plurality of dielectric structures are patterned in different forms within neighboring pixels on the common electrode.

Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention. The objectives and other advantages of the

invention will be realized and attained by the scheme particularly pointed out in the written description and claims hereof as well as the appended drawings.

To achieve these and other advantages and in accordance with the purpose of the present invention, as embodied and broadly described, a multi-domain liquid crystal display device according to the present invention includes: first and second substrates opposing each other; a plurality of gate lines and data lines formed on the first substrate lengthwise and crosswise, for defining a pixel region; a common auxiliary electrode formed on a layer equal to the gate lines to surround the pixel region; a gate insulating film formed on the first substrate; a passivation film formed on the gate insulating film including the first substrate; a pixel electrode formed in the pixel region; a light-shielding layer formed on the second substrate; a color filter layer formed on the light-shielding layer; a common electrode formed on the color filter layer; a plurality of electric field distortion dielectric structures patterned in different forms within neighboring pixels; an alignment film formed at least one of the first and second substrates; and a liquid crystal layer formed between the first substrate and the second substrate.

The multi-domain liquid crystal display device of the present invention is characterized in that an electric field dielectric structure is additionally formed on the pixel electrode or an electric field induction window is formed within

the dielectric structure. The dielectric structure has a constant smaller than that of the liquid crystal layer and is made of photosensitive material, and preferably, photoacrylate or benzocyclobutene(BCB). The liquid crystal has a positive
5 dielectric anisotropy or a negative dielectric anisotropy, and the liquid crystal layer may include a chiral dopant.

It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention will be described in detail with reference to the following drawings in which like reference numerals refer to like elements wherein:

Figs. 1a and 1b are sectional views showing a related art liquid crystal display device;

Fig. 2a is a plane view showing a multi-domain liquid crystal display device according to the first embodiment of the present invention;

Figs. 2b to 2e are sectional views showing a multi-domain liquid crystal display device according to the first embodiment of the present invention;

Fig. 3a is a plane view showing a multi-domain liquid crystal display device according to the second embodiment of the

present invention;

Figs. 3b to 3e are sectional views showing a multi-domain liquid crystal display device according to the second embodiment of the present invention;

5 Fig. 4a is a plane view showing a multi-domain liquid crystal display device according to the third embodiment of the present invention;

10 Figs. 4b to 4e are sectional views showing a multi-domain liquid crystal display device according to the third embodiment of the present invention;

Fig. 5a is a plane view showing a multi-domain liquid crystal display device according to the fourth embodiment of the present invention;

15 Figs. 5b to 5e are sectional views showing a multi-domain liquid crystal display device according to the fourth embodiment of the present invention;

Fig. 6a is a plane view showing a multi-domain liquid crystal display device according to the fifth embodiment of the present invention;

20 Figs. 6b to 6e are sectional views showing a multi-domain liquid crystal display device according to the fifth embodiment of the present invention;

25 Figs. 7a to 7e are plane views showing a multi-domain liquid crystal display device according to the sixth embodiment of the present invention;

Figs. 8a to 8e are plane views showing a multi-domain liquid crystal display device according to the seventh embodiment of the present invention;

Figs. 9a to 9e are plane views showing a multi-domain liquid crystal display device according to the eighth embodiment of the present invention;

Figs. 10a to 10e are plane views showing a multi-domain liquid crystal display device according to the ninth embodiment of the present invention; and

Figs. 11a to 11e are plane views showing a multi-domain liquid crystal display device according to the tenth embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Reference will now be made in detail to the preferred embodiments of the present invention, examples of which are illustrated in the accompanying drawings.

A multi-domain liquid crystal display device of the present invention will be described with the accompanying drawings.

The multi-domain liquid crystal display device includes a first substrate 21, a second substrate 33, a plurality of data lines 3 and gate lines 1, a common auxiliary electrode 15, a thin film transistor, a passivation film 37, and a pixel electrode 13.

The data lines 3 and gate lines 1 are formed on the first substrate 21 lengthwise and crosswise to divide the first

substrate into a plurality of pixel regions. The common auxiliary electrode 15 is formed on a layer equal to the gate lines to distort electric field. The thin film transistor is formed in each pixel region on the first substrate and includes a gate electrode 11, a gate insulating film 35, a semiconductor layer 5, an ohmic contact layer, and source/drain electrodes 7 and 9. The passivation film 37 is formed on an entire surface of the first substrate 31. The pixel electrode 13 is formed on the passivation film 37 to be connected with the drain electrode 9.

The multi-domain liquid crystal display device further includes a light-shielding layer 25, a color filter layer 23 formed on the light-shielding layer 25, a common electrode 17 formed on the color filter layer 25, and a liquid crystal layer formed between the first substrate 31 and the second substrate 33. The light-shielding layer 25 is formed on the second substrate 33 to shield light leaked from the gate lines 1, the data lines 3 and the thin film transistor.

Dielectric structures 53 patterned in at least two different forms within neighboring pixels are formed on the common electrode 17. An electric field induction window 51 having a hole or slit shape is formed within the pixel electrode 13.

To fabricate the aforementioned multi-domain liquid crystal display device, the thin film transistor consisting of the gate electrode 11, the gate insulating film 35, the semiconductor layer 5, the ohmic contact layer and the source/drain electrodes

7 and 9 is formed in each pixel region of the first substrate. At this time, the plurality of gate lines 1 and data lines 3 are formed to divide the first substrate into a plurality of pixel regions.

5 The gate electrode 11 and the gate lines 1 are formed in such a manner that metals such as Al, Mo, Cr, Ta, Al alloy or their alloys are layered by sputtering and patterned. At the same time, the common auxiliary electrode 15 is formed to surround the pixel region. Then, the gate insulating film 35 is formed in such
10 a manner that SiN_x or SiO_x is deposited on the common auxiliary electrode 15 by plasma enhancement chemical vapor deposition (PECVD) method and patterned. Subsequently, the semiconductor layer 5 and the ohmic contact layer are formed in such a manner that a-Si and n^+ a-Si are deposited by PECVD method and
15 patterned. Furthermore, the gate insulating film 35, a-Si and n^+ a-Si may successively be deposited and patterned. Metals such as Al, Mo, Cr, Ta, Al alloy or their alloys are layered by sputtering and patterned so that the data lines 3 and the source/drain electrodes 7 and 9 are formed.

20 At this time, a storage electrode is formed to overlap the gate lines 1 and/or the common auxiliary electrode 15. The storage electrode acts as a storage capacitor together with the gate lines 1 and/or the common auxiliary electrode 15.

Subsequently, the passivation film 37 is formed of a
25 material BCB(BenzoCycloButene), acrylic resin, polyimide, SiN_x or

SiO_x on the first substrate 31. A metal such as indium tin oxide (ITO), Al or Cr is deposited by sputtering and patterned to form the pixel electrode 13. At this time, the pixel electrode 13 is connected with the drain electrode and the storage electrode through a contact hole and patterned in various forms to form an electric field induction window 51 therein.

When the common auxiliary electrode 15 is formed of the same material as the gate lines 1, it is formed on the same layer as the gate lines 1 and connected with the common electrode 17 using one mask. An additional mask may be used to form other metal or different double layers.

Additionally, the common auxiliary electrode 15 and the pixel electrode 13 may be overlapped with each other and vice versa. At this time, the common auxiliary electrode 15 and the pixel electrode 13 forms a storage capacitor.

The light-shielding layer 25 is formed on the second substrate 33, and the color filter layer 23 is formed to repeat R(red), G(green) and B(blue) elements for each pixel. The common electrode 17 is formed of a transparent electrode such as ITO on the color filter layer 23, in the same manner as the pixel electrode 13. A photoresist material is deposited on the common electrode 17 and patterned by photolithography to form dielectric structures 53 having various shapes. The dielectric structures are patterned in different shapes within neighboring pixels and two or more pixels are formed to repeat in one unit. Thus, a

multi-domain is realized.

Subsequently, a liquid crystal is injected between the first substrate 31 and the second substrate 33 so that a multi-domain liquid crystal display device is completed.

5 Preferably, the dielectric structures 53 have dielectric constants equal to or smaller than the liquid crystal layer, and more preferably 3 or below. A material such as photoacrylate or BCB may be used as the dielectric structures.

10 To apply a voltage V_{com} to the common auxiliary electrode 15, an Ag-Dotting portion is formed in each corner of a driving region of the liquid crystal display device on the first substrate 31, and the electric field is applied to the second substrate 33 to drive the liquid crystal by the potential difference between upper and lower substrates. The Ag-Dotting
15 portion of each corner is connected with the common auxiliary electrode 15. Thus, the voltage V_{com} is applied to the common auxiliary electrode 15. This process is performed when forming the common auxiliary electrode 15.

20 High molecules are formed on at least one of the first substrate 31 and the second substrate 33 so that a phase difference film 29 is formed.

The phase difference film 29 is a negative uniaxial film and acts to compensate a viewing angle of a user.

25 Therefore, a region having no gray inversion is expanded, contrast ratio in incline direction increases, and a multi-domain

is formed by one pixel. Thus, a viewing angle in left and right direction can effectively be compensated.

In addition to the negative uniaxial film, a negative biaxial film may be formed as the phase difference film. The negative biaxial film having two axes can obtain viewing angle characteristic wider than the negative uniaxial film.

Subsequently, a polarizer(not shown) is attached on both substrates. The polarizer may be formed in an integral form with the phase difference film.

In the multi-domain liquid crystal display device of Fig. 2, the common auxiliary electrode 15 is formed to surround the pixel region. The dielectric structure in one pixel is formed in horizontal direction while the dielectric structure in a neighboring pixel is formed in vertical direction. Thus, four domains are formed by a simple pattern.

In Figs. 2b and 2d, the passivation film 37 is formed of a material such as SiN_x or SiO_x . In Figs. 2c and 2e, the passivation film 37 is formed of BCB, acrylic resin or polyimide.

In the multi-domain liquid crystal display device of Figs. 2d and 2e, the pixel electrode 13 overlaps the common auxiliary electrode 15 to form a storage capacitor. The light-shielding layer 25 also overlaps the common auxiliary electrode. Accordingly, an aperture ratio is high. In the multi-domain liquid crystal display device shown in Figs. 2b and 2c, the pixel electrode 13 does not overlap the common auxiliary electrode 15

and the light-shielding layer 25 overlaps the pixel electrode so that leakage of light is avoided.

In the multi-domain liquid crystal display device shown in Fig. 3, the common auxiliary electrode 15 is formed to surround the pixel region. The dielectric structure 53 in one pixel is formed in horizontal direction while the dielectric structure in a neighboring pixel is formed in vertical direction. Moreover, an electric field induction window 51 of a hole or slit is formed in the pixel electrode 13. Thus, the multi-domain liquid crystal display device having an improved characteristic as compared with the first embodiment is obtained.

Furthermore, in Figs. 3b and 3d, the passivation film 37 is formed of a material such as SiN_x or SiO_x . In Figs. 3c and 3e, the passivation film 37 is formed of BCB, acrylic resin or polyimide.

In the multi-domain liquid crystal display device of Figs. 3d and 3e, the pixel electrode 13 overlaps the common auxiliary electrode 15 to form a storage capacitor. The light-shielding layer 25 also overlaps the common auxiliary electrode. Accordingly, an aperture ratio is high. In the multi-domain liquid crystal display device shown in Figs. 3b and 3c, the pixel electrode 13 does not overlap the common auxiliary electrode 15 and the light-shielding layer 25 overlaps the pixel electrode so that leakage of light is avoided.

In the multi-domain liquid crystal display device shown in

Fig. 4, the common auxiliary electrode 15 is formed to surround the pixel region. The dielectric structure 53 in one pixel is formed in horizontal direction while the dielectric structure in a neighboring pixel is formed in vertical direction. Moreover, two or more electric field induction windows 51 of holes or slits are formed in the pixel electrode 13. Thus, the multi-domain liquid crystal display device of Fig. 4 has an improved characteristic as compared with the second embodiment.

Furthermore, in Figs. 4b and 4d, the passivation film 37 is formed of a material such as SiN_x or SiO_x . In Figs. 4c and 4e, the passivation film 37 is formed of BCB, acrylic resin or polyimide.

In the multi-domain liquid crystal display device of Figs. 4d and 4e, the pixel electrode 13 overlaps the common auxiliary electrode 15 to form a storage capacitor. The light-shielding layer 25 also overlaps the common auxiliary electrode. Accordingly, an aperture ratio is high. In the multi-domain liquid crystal display device shown in Figs. 4b and 4c, the pixel electrode 13 does not overlap the common auxiliary electrode 15 and the light-shielding layer 25 overlaps the pixel electrode so that leakage of light is avoided.

In the multi-domain liquid crystal display device shown in Fig. 5, the dielectric structure 53 in one pixel is formed in horizontal direction and an electric field induction window 51 of a hole or slit is formed in the pixel electrode 13. The common

auxiliary electrode 15 is formed in a region where the electric field induction window is formed and around the pixel region. Thus, the multi-domain liquid crystal display device having an improved characteristic as compared with the above embodiments is obtained.

Furthermore, in Figs. 5b and 5d, the passivation film 37 is formed of a material such as SiN_x or SiO_x . In Figs. 5c and 5e, the passivation film 37 is formed of BCB, acrylic resin or polyimide.

In the multi-domain liquid crystal display device of Figs. 5d and 5e, the pixel electrode 13 overlaps the common auxiliary electrode 15 to form a storage capacitor. The light-shielding layer 25 also overlaps the common auxiliary electrode. Accordingly, an aperture ratio is high. In the multi-domain liquid crystal display device shown in Figs. 5b and 5c, the pixel electrode 13 does not overlap the common auxiliary electrode 15 and the light-shielding layer 25 overlaps the pixel electrode so that leakage of light is avoided.

In the multi-domain liquid crystal display device shown in Fig. 6, the dielectric structure 53 in one pixel is formed in horizontal direction while the dielectric structure in a neighboring pixel is formed in vertical direction. Moreover, two or more electric field induction windows 51 of holes or slits are formed in the pixel electrode 13. The common auxiliary electrode 15 is formed in a region where the electric field induction

windows are formed and around the pixel region. Thus, the multi-domain liquid crystal display device of Fig. 4 has an improved characteristic as compared with the fourth embodiment.

Furthermore, in Figs. 6b and 6d, the passivation film 37 is formed of a material such as SiN_x or SiO_x . In Figs. 6c and 6e, the passivation film 37 is formed of BCB, acrylic resin or polyimide.

In the multi-domain liquid crystal display device of Figs. 6d and 6e, the pixel electrode 13 overlaps the common auxiliary electrode 15 to form a storage capacitor. The light-shielding layer 25 also overlaps the common auxiliary electrode. Accordingly, an aperture ratio is high. In the multi-domain liquid crystal display device shown in Figs. 6b and 6c, the pixel electrode 13 does not overlap the common auxiliary electrode 15 and the light-shielding layer 25 overlaps the pixel electrode so that leakage of light is avoided.

Figs. 7, 8, 9, 10 and 11 show plane views of a multi-domain liquid crystal display device according to the sixth to tenth embodiments of the present invention.

In the liquid crystal display device shown in Figs. 7, 8, 9, 10 and 11, three pixels repeat alternately in one unit and the dielectric structure is simply formed in horizontal direction or vertical direction. Thus, a multi-domain is realized.

In addition to the above embodiments, the liquid crystal display device of the present invention, the dielectric structure

53 is formed on the pixel electrode, the common electrode, the color filter layer and/or an overcoat layer. The pixel electrode, the passivation film, the gate insulating film, the color filter layer, the overcoat layer and/or the common electrode are patterned so that the electric field induction window 51 such as hole or slit is formed therein. Thus, electric distortion effect and the multi-domain can be realized.

Furthermore, the electric field induction window 51 or the dielectric structure 53 is patterned long in horizontal direction, vertical direction and diagonal direction to obtain effect divided into two domains, or patterned in X shape, + shape, \diamond shape, comb shape, or double Y(YY) shape, or X shape and + shape are simultaneously patterned, to obtain effect divided into four domains and multi-domain. Alternatively, the electric field induction window 51 or the dielectric structure 53 may be formed on at least one of the first substrate and the second substrate, or independently or together on both substrates.

Additionally, in the multi-domain liquid crystal display device of the present invention, an alignment film (not shown) is formed over the first substrate and/or the second substrate. A photo alignment film of a material such as PVCN, PSCN, CelCN, or their based compound may be used as the alignment film. The other materials suitable for photo-alignment may be used as the alignment film.

Light is irradiated to the photo-alignment film at least one time to determine a pretilt angle and alignment direction or pretilt direction of the director of the liquid crystal molecule at the same time, thereby obtaining stable alignment of the liquid crystal. The light used for the photo-alignment is suitable for light in an ultraviolet ray region. Un-polarized light, linearly-polarized light or partially polarized light may be used for the photo-alignment.

The photo-alignment is applicable to one of the first substrate and the second substrate or both substrates. Different alignment methods are applicable to both substrates. Although the alignment film has been formed, alignment process may not be performed.

Furthermore, the aforementioned alignment is performed to form the multi-domain liquid crystal display device divided into at least two regions. Thus, the liquid crystal molecule of the liquid crystal layer may be aligned differently on each region. In other words, each pixel is divided into four regions in + shape or X shape, or each pixel is divided in horizontal, vertical, or diagonal direction. Alignment process or alignment direction is varied depending on each region and each substrate, so that multi-domain effect can be realized. At least one region of the divided regions may be a non-alignment region or all the divided regions may be a non-alignment region.

As aforementioned, the multi-domain liquid crystal display

device has the following advantages.

The common auxiliary electrode is formed on the same layer as the gate lines to surround the pixel region and the dielectric structures are patterned in different forms within neighboring pixels on the common electrode, so that electric field distortion can be induced, thereby facilitating control of alignment direction in the domain and improving the viewing angle and the multi-domain effect. Moreover, since the common auxiliary electrode is in the same layer as the gate lines, short between the pixel electrode and the common auxiliary electrode is avoided, thereby improving yield.

The foregoing embodiments are merely exemplary and are not to be construed as limiting the present invention. The present teachings can be readily applied to other types of apparatuses. The description of the present invention is intended to be illustrative, and not to limit the scope of the claims. Many alternatives, modifications, and variations will be apparent to those skilled in the art.

What is claimed is:

1. A multi-domain liquid crystal display device comprising:
first and second substrates opposing each other between a
liquid crystal layer;

5 a plurality of gate lines and data lines on the first
substrate lengthwise and crosswise, to define a pixel region;

a common auxiliary electrode on a layer equal to the gate
lines to surround the pixel region;

a gate insulating film on the first substrate;

10 a passivation film on the gate insulating film including the
first substrate;

a pixel electrode in the pixel region;

a light-shielding layer on the second substrate;

a color filter layer on the light-shielding layer;

15 a common electrode on the color filter layer;

a plurality of electric field distortion dielectric
structures patterned in different forms within neighboring
pixels; and

20 an alignment film on at least one of the first and second
substrates.

2. The multi-domain liquid crystal display device as claimed
in claim 1, wherein the pixel electrode overlaps the common
auxiliary electrode.

3. The multi-domain liquid crystal display device as claimed in claim 2, wherein the light-shielding layer overlaps the common auxiliary electrode.

4. The multi-domain liquid crystal display device as claimed in claim 1, wherein the pixel electrode does not overlap the common auxiliary electrode.

5. The multi-domain liquid crystal display device as claimed in claim 4, wherein the light-shielding layer overlaps the pixel electrode.

6. The multi-domain liquid crystal display device as claimed in claim 4, wherein the gate insulating film and the passivation film are formed in a region except the common auxiliary electrode.

7. The multi-domain liquid crystal display device as claimed in claim 1, wherein the common auxiliary electrode is electrically connected with the common electrode.

8. The multi-domain liquid crystal display device as claimed in claim 1, further comprising an L-shaped thin film transistor formed in a crossing point of the gate lines and the data lines.

9. The multi-domain liquid crystal display device as claimed in claim 1, wherein the electric field dielectric structures are formed on the pixel electrode.

10. The multi-domain liquid crystal display device as
5 claimed in claim 1, wherein the electric field dielectric structures are formed on the common electrode.

11. The multi-domain liquid crystal display device as
10 claimed in claim 1, wherein the electric field dielectric structures are formed on the color filter layer.

12. The multi-domain liquid crystal display device as claimed in claim 1, wherein the pixel electrode has an electric field induction window therein.

13. The multi-domain liquid crystal display device as
15 claimed in claim 1, wherein the passivation film has an electric field induction window therein.

14. The multi-domain liquid crystal display device as claimed in claim 1, wherein the gate insulating film has an electric field induction window therein.

20 15. The multi-domain liquid crystal display device as

claimed in claim 1, wherein the common electrode has an electric field induction window therein.

16. The multi-domain liquid crystal display device as claimed in claim 1, wherein the color filter layer has an electric field induction window thereon.

17. The multi-domain liquid crystal display device as claimed in claim 1, further comprising an over coat layer on the color filter layer.

18. The multi-domain liquid crystal display device as claimed in claim 17, wherein the over coat layer has an electric field induction window therein.

19. The multi-domain liquid crystal display device as claimed in claim 1, wherein the passivation film comprises a material selected from one group consisting of BCB, acrylic resin and polyimide.

20. The multi-domain liquid crystal display device as claimed in claim 1, wherein the passivation film comprises a material selected from one group consisting of SiN_x , and SiO_x .

21. The multi-domain liquid crystal display device as

claimed in claim 1, wherein the common auxiliary electrode comprises a material selected from one group consisting of ITO, Al, Mo, Cr, Ta, Ti and Al alloy.

22. The multi-domain liquid crystal display device as
5 claimed in claim 1, wherein the pixel electrode comprises a material selected from one group consisting of ITO, Al and Cr.

23. The multi-domain liquid crystal display device as claimed in claim 1, wherein the common electrode comprises ITO.

24. The multi-domain liquid crystal display device as
10 claimed in claim 1, wherein the dielectric structure has a dielectric constant smaller than that of the liquid crystal layer.

25. The multi-domain liquid crystal display device as
15 claimed in claim 1, wherein the dielectric structure comprises a photosensitive material.

26. The multi-domain liquid crystal display device as claimed in claim 1, wherein the dielectric structure comprises a material selected from one group consisting of photoacrylate and BCB.

27. The multi-domain liquid crystal display device as claimed in claim 1, wherein the pixel region is divided into at least two regions so that liquid crystal molecules of the liquid crystal layer have different driving characteristics on each region.

28. The multi-domain liquid crystal display device as claimed in claim 1, wherein the alignment film is divided into at least two regions so that liquid crystal molecules of the liquid crystal layer have different alignment characteristics on each region.

29. The multi-domain liquid crystal display device as claimed in claim 28, wherein at least one region of the alignment film is aligned.

30. The multi-domain liquid crystal display device as claimed in claim 28, wherein the regions of the alignment film are not aligned.

31. The multi-domain liquid crystal display device as claimed in claim 1, wherein the liquid crystal constituting the liquid crystal layer has a positive dielectric anisotropy or a negative dielectric anisotropy.

32. The multi-domain liquid crystal display device as claimed in claim 1, further comprising a negative uniaxial film on at least one of the first substrate and the second substrate.

33. The multi-domain liquid crystal display device as
5 claimed in claim 1, further comprising a negative biaxial film on at least one of the first substrate and the second substrate.

34. The multi-domain liquid crystal display device as claimed in claim 1, wherein the liquid crystal layer includes a chiral dopant.

35. A multi-domain liquid crystal display device comprising:
10 a data line to apply a data signal;
a pixel electrode for driving a liquid crystal;
a gate line crossed to the data line, to define a pixel region;

15 a common auxiliary electrode formed to surround the pixel region; and

a plurality of electric field distortion dielectric structures formed in different forms within neighboring pixels.

36. The multi-domain liquid crystal display device as
20 claimed in claim 35, wherein the common auxiliary electrode is formed on a layer same as the gate line.

37. The multi-domain liquid crystal display device as claimed in claim 35, wherein the pixel electrode has an electric field induction window therein.

ABSTRACT OF THE DISCLOSURE

A multi-domain liquid crystal display device includes: first and second substrates opposing each other; a plurality of gate lines and data lines on the first substrate lengthwise and crosswise, to define a pixel region; a common auxiliary electrode on a layer equal to the gate lines to surround the pixel region; a gate insulating film on the first substrate; a passivation film on the gate insulating film including the first substrate; a pixel electrode in the pixel region; a light-shielding layer on the second substrate; a color filter layer on the light-shielding layer; a common electrode on the color filter layer; a plurality of electric field distortion dielectric structures patterned in different forms within neighboring pixels; an alignment film on at least one of the first and second substrates; and a liquid crystal layer between the first substrate and the second substrate.

FIG.1A
prior art

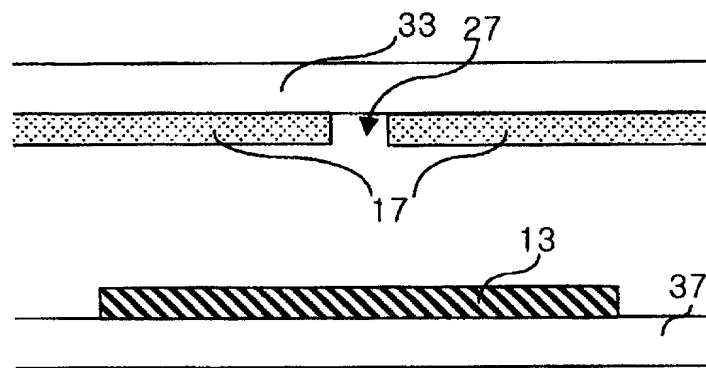


FIG.1A
prior art

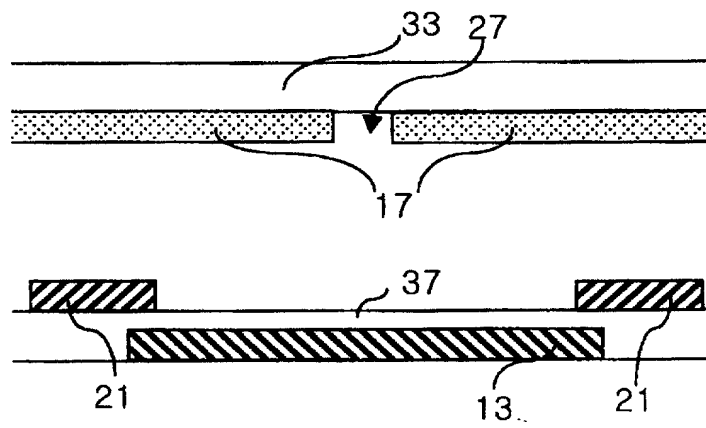


FIG.2A

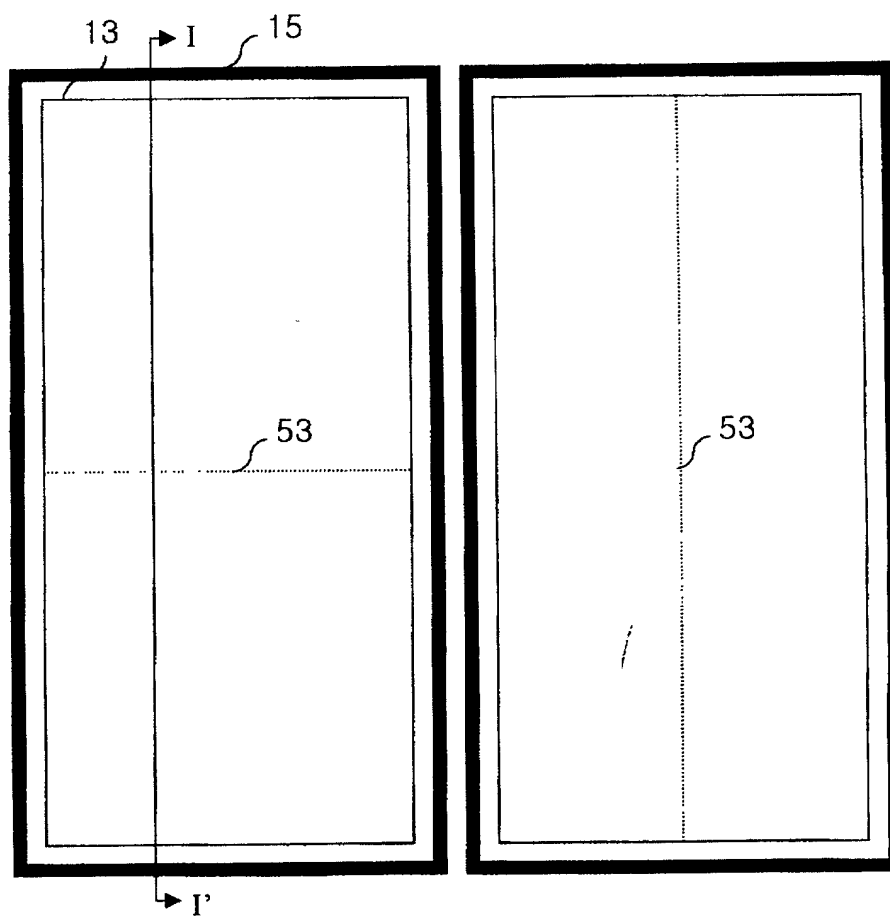


FIG. 2B

13

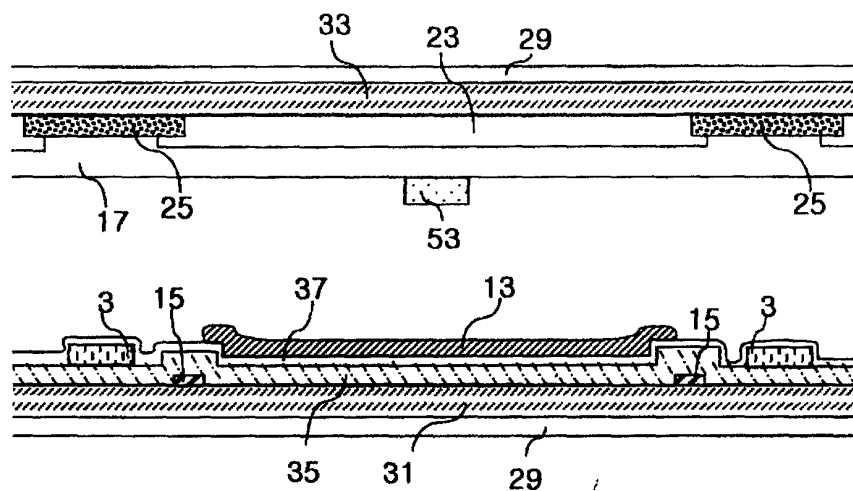


FIG. 2C

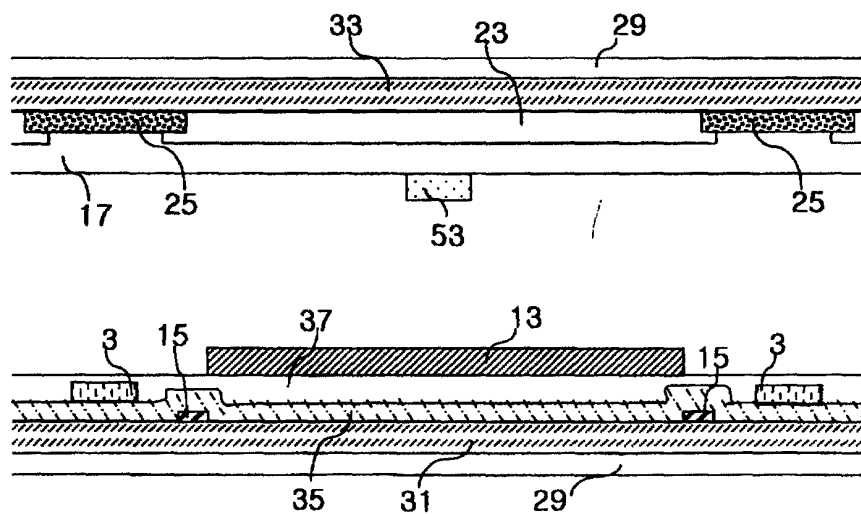


FIG.2D

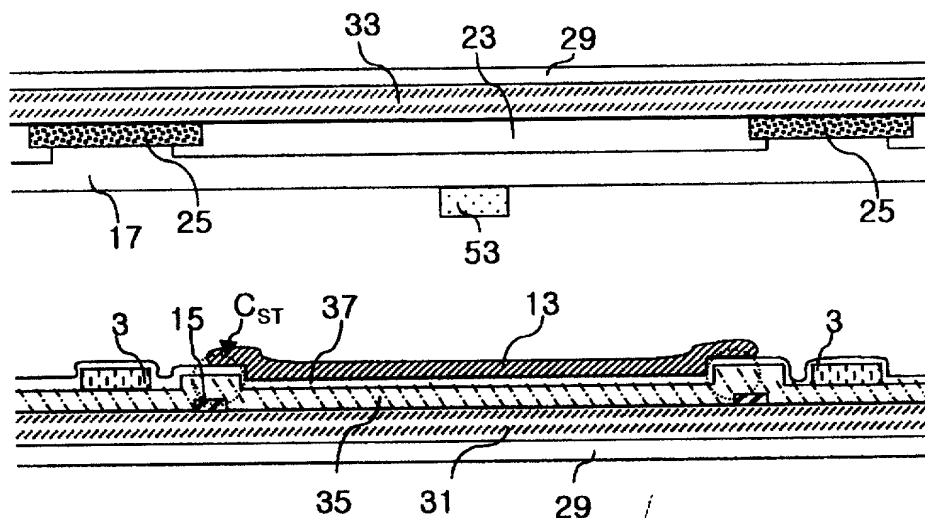


FIG.2E

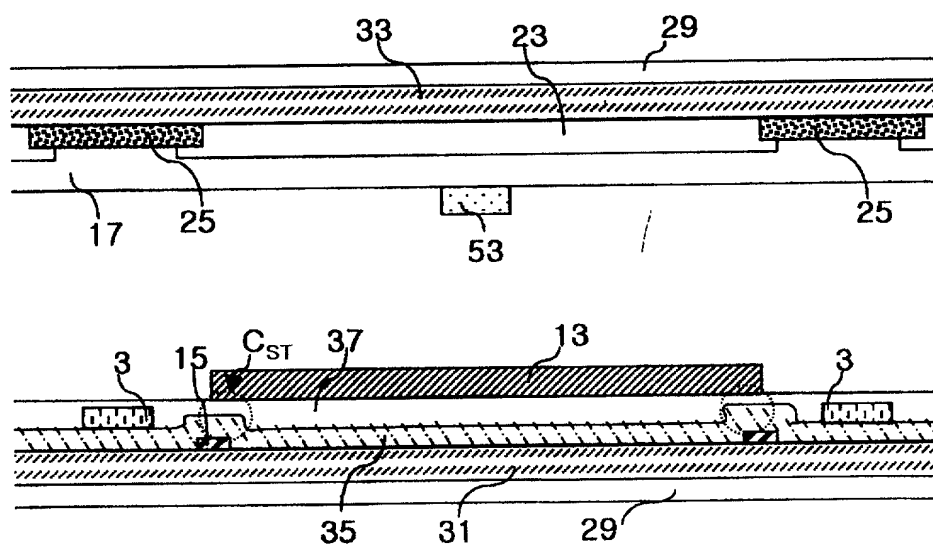


FIG. 3A

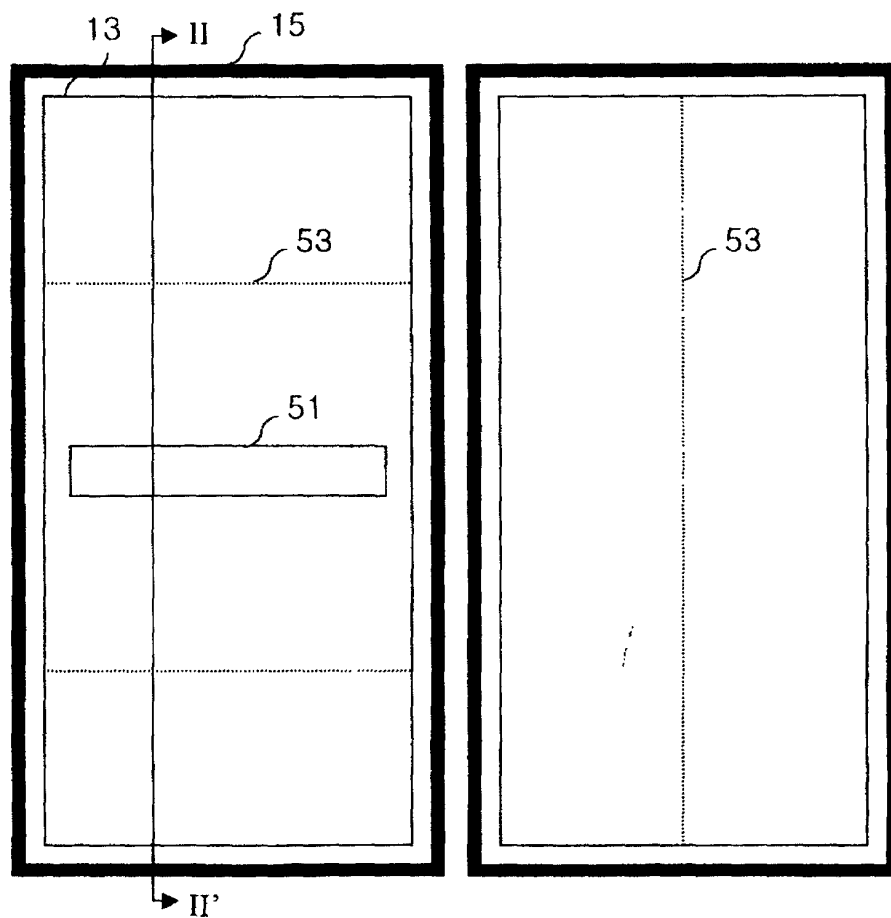


FIG. 3B

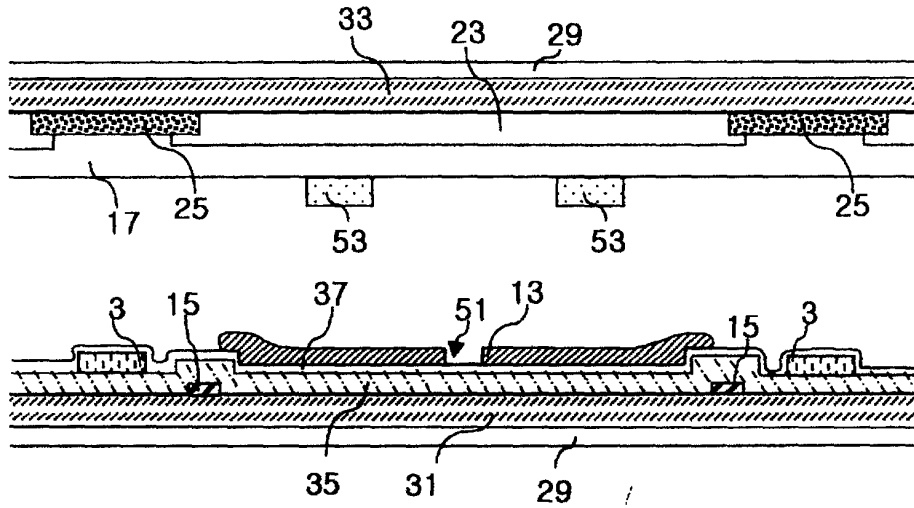


FIG. 3C

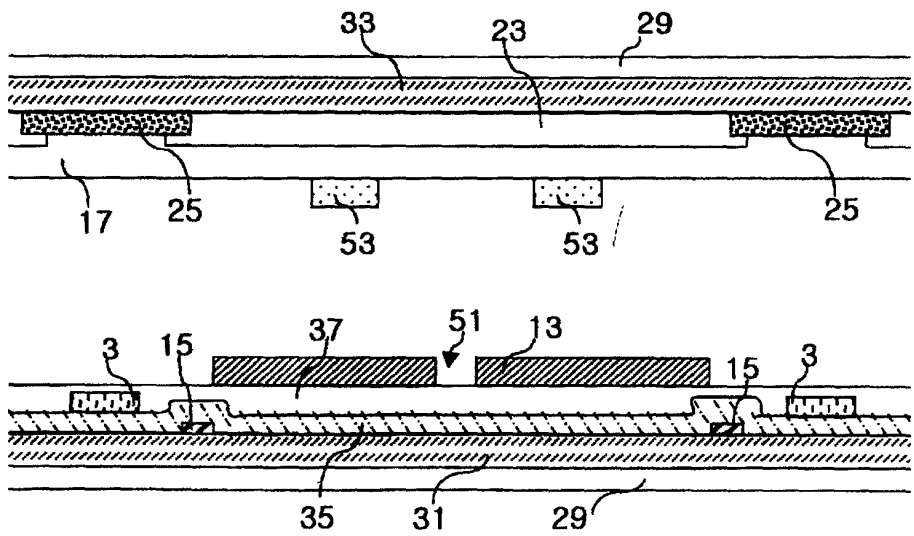


FIG. 3D

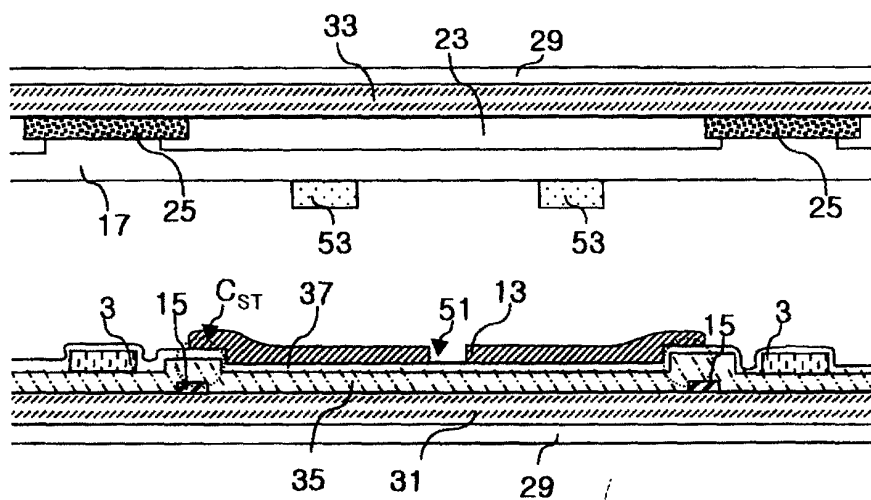


FIG. 3E

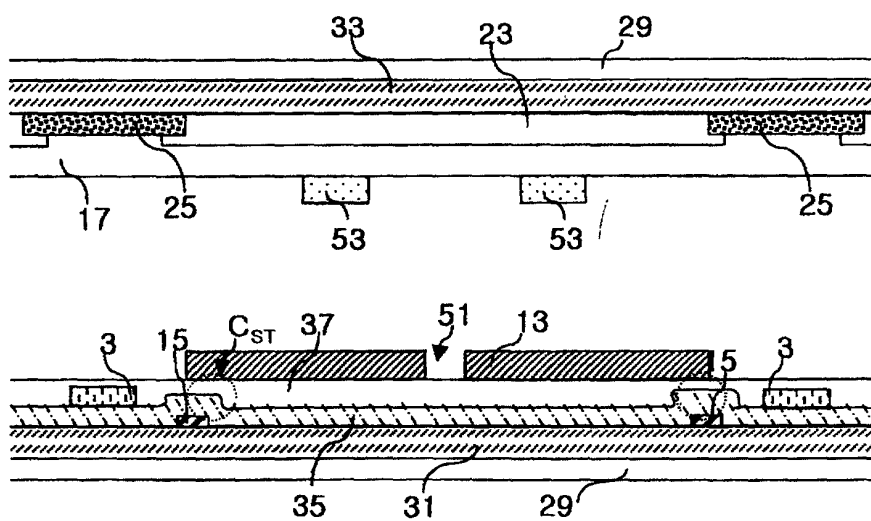


FIG. 4A

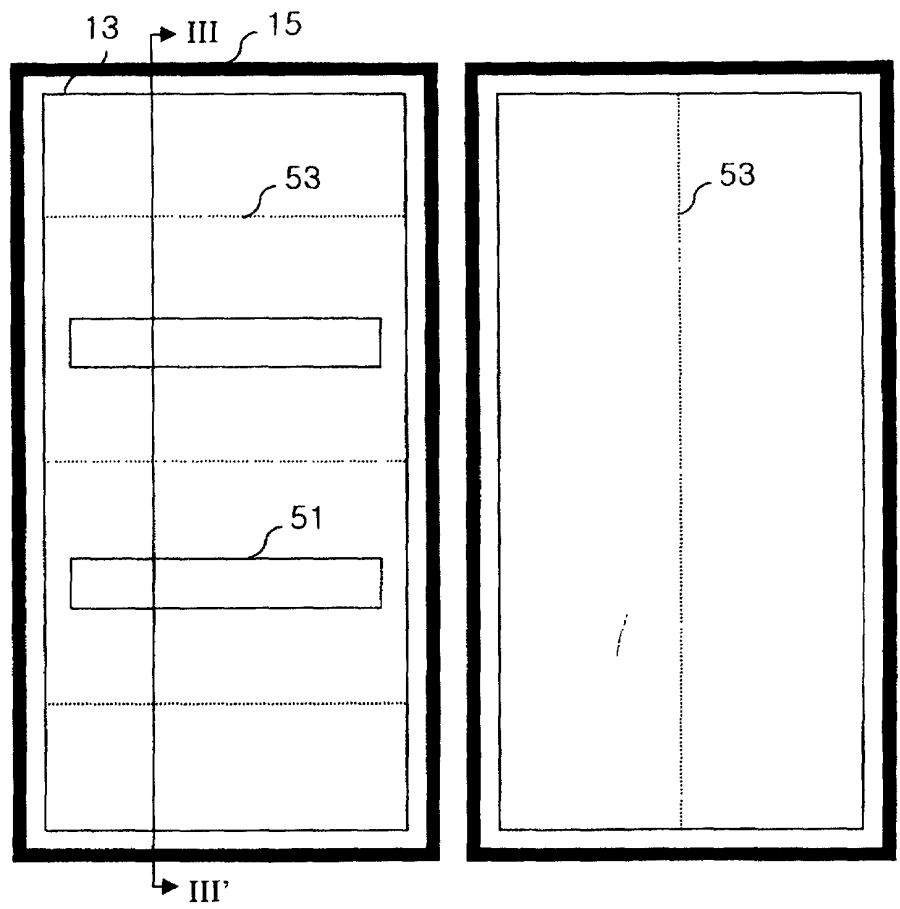


FIG. 4B

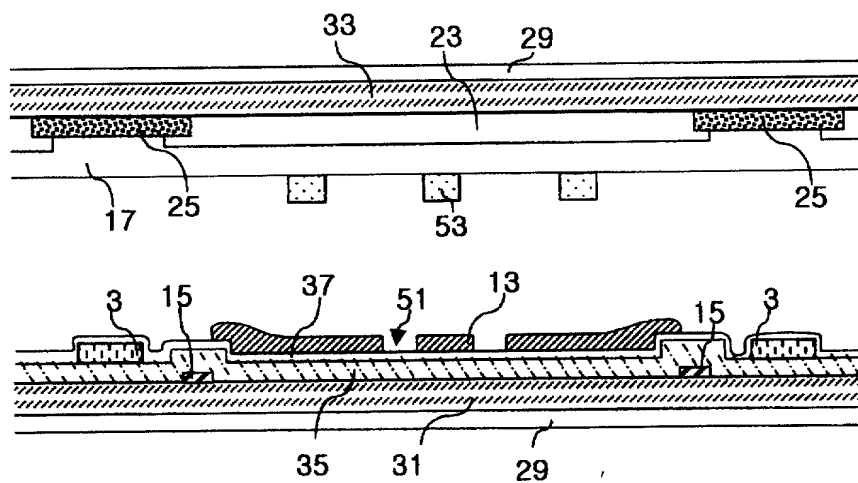
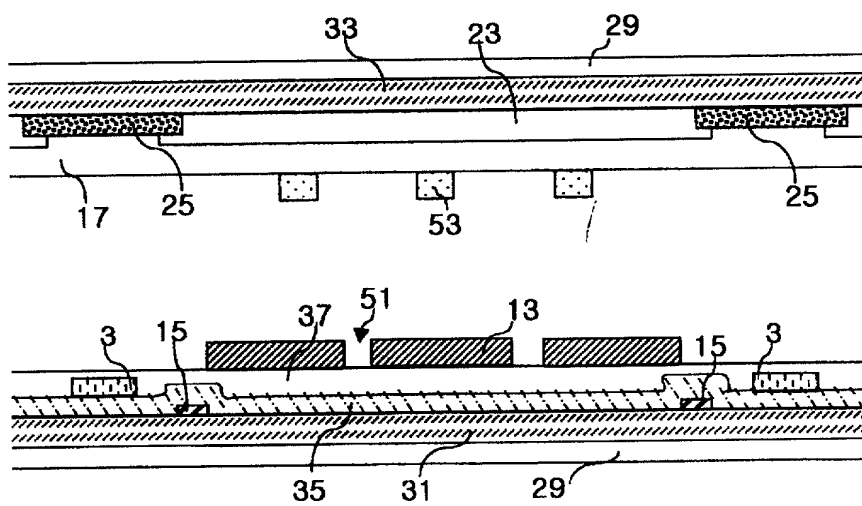
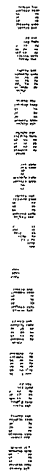


FIG. 4C



[illegible]

Parameter	Unit	Value	Unit	Value
Temperature	°C	25.0	°C	25.0
Pressure	atm	1.0	atm	1.0
Flow rate	L/min	1.0	L/min	1.0
Concentration	g/L	1.0	g/L	1.0
Time	min	10	min	10
Volume	L	1.0	L	1.0
Mass	g	1.0	g	1.0
Area	cm ²	1.0	cm ²	1.0
Length	cm	1.0	cm	1.0
Width	cm	1.0	cm	1.0
Height	cm	1.0	cm	1.0
Depth	cm	1.0	cm	1.0
Radius	cm	1.0	cm	1.0
Diameter	cm	1.0	cm	1.0
Volume	L	1.0	L	1.0
Mass	g	1.0	g	1.0
Area	cm ²	1.0	cm ²	1.0
Length	cm	1.0	cm	1.0
Width	cm	1.0	cm	1.0
Height	cm	1.0	cm	1.0
Depth	cm	1.0	cm	1.0
Radius	cm	1.0	cm	1.0
Diameter	cm	1.0	cm	1.0
Volume	L	1.0	L	1.0
Mass	g	1.0	g	1.0
Area	cm ²	1.0	cm ²	1.0
Length	cm	1.0	cm	1.0
Width	cm	1.0	cm	1.0
Height	cm	1.0	cm	1.0
Depth	cm	1.0	cm	1.0
Radius	cm	1.0	cm	1.0
Diameter	cm	1.0	cm	1.0
Volume	L	1.0	L	1.0
Mass	g	1.0	g	1.0
Area	cm ²	1.0	cm ²	1.0
Length	cm	1.0	cm	1.0
Width	cm	1.0	cm	1.0
Height	cm	1.0	cm	1.0
Depth	cm	1.0	cm	1.0
Radius	cm	1.0	cm	1.0
Diameter	cm	1.0	cm	1.0
Volume	L	1.0	L	1.0
Mass	g	1.0	g	1.0
Area	cm ²	1.0	cm ²	1.0
Length	cm	1.0	cm	1.0
Width	cm	1.0	cm	1.0
Height	cm	1.0	cm	1.0
Depth	cm	1.0	cm	1.0
Radius	cm	1.0	cm	1.0
Diameter	cm	1.0	cm	1.0
Volume	L	1.0	L	1.0
Mass	g	1.0	g	1.0
Area	cm ²	1.0	cm ²	1.0
Length	cm	1.0	cm	1.0
Width	cm	1.0	cm	1.0
Height	cm	1.0	cm	1.0
Depth	cm	1.0	cm	1.0
Radius	cm	1.0	cm	1.0
Diameter	cm	1.0	cm	1.0
Volume	L	1.0	L	1.0
Mass	g	1.0	g	1.0
Area	cm ²	1.0	cm ²	1.0
Length	cm	1.0	cm	1.0
Width	cm	1.0	cm	1.0
Height	cm	1.0	cm	1.0
Depth	cm	1.0	cm	1.0
Radius	cm	1.0	cm	1.0
Diameter	cm	1.0	cm	1.0
Volume	L	1.0	L	1.0
Mass	g	1.0	g	1.0
Area	cm ²	1.0	cm ²	1.0
Length	cm	1.0	cm	1.0
Width	cm	1.0	cm	1.0
Height	cm	1.0	cm	1.0
Depth	cm	1.0	cm	1.0
Radius	cm	1.0	cm	1.0
Diameter	cm	1.0	cm	1.0
Volume	L	1.0	L	1.0
Mass	g	1.0	g	1.0
Area	cm ²	1.0	cm ²	1.0
Length	cm	1.0	cm	1.0
Width	cm	1.0	cm	1.0
Height	cm	1.0	cm	1.0
Depth	cm	1.0	cm	1.0
Radius	cm	1.0	cm	1.0
Diameter	cm	1.0	cm</	

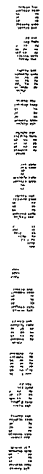


FIG. 5A

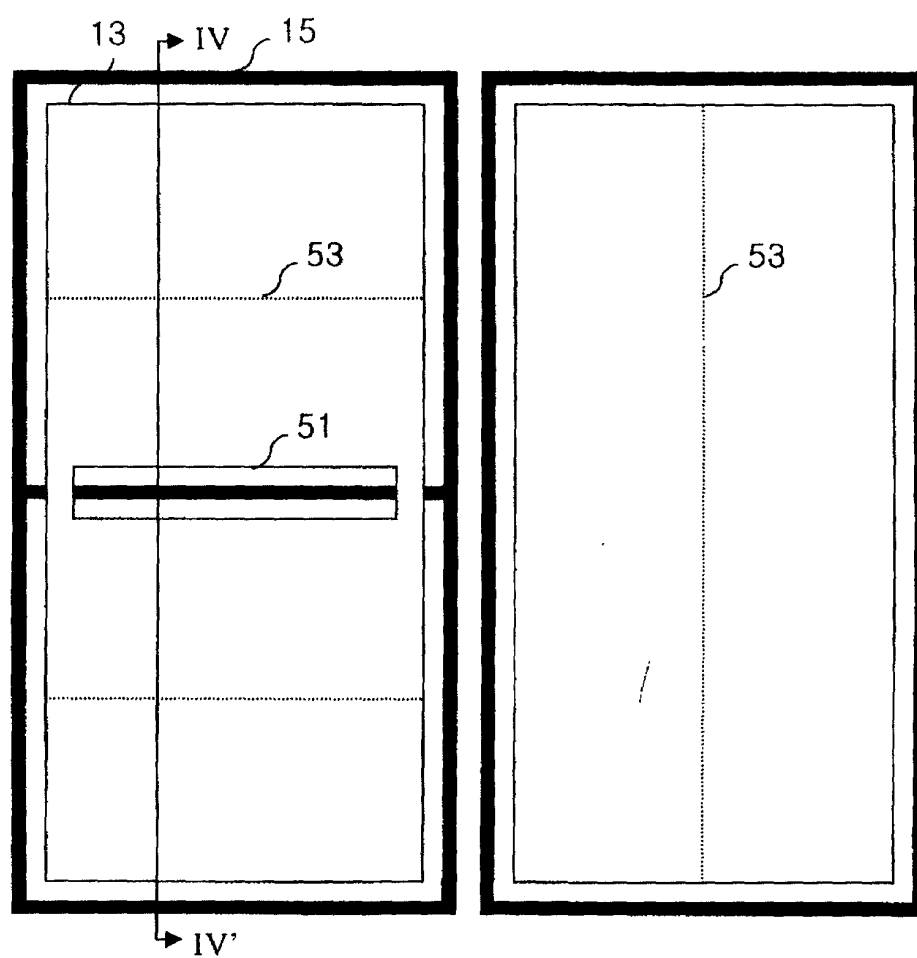


FIG. 5B

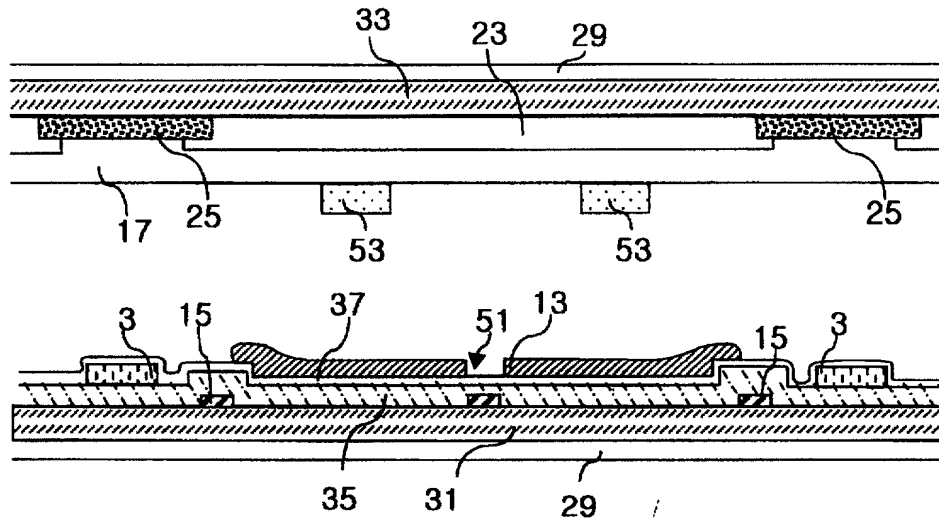
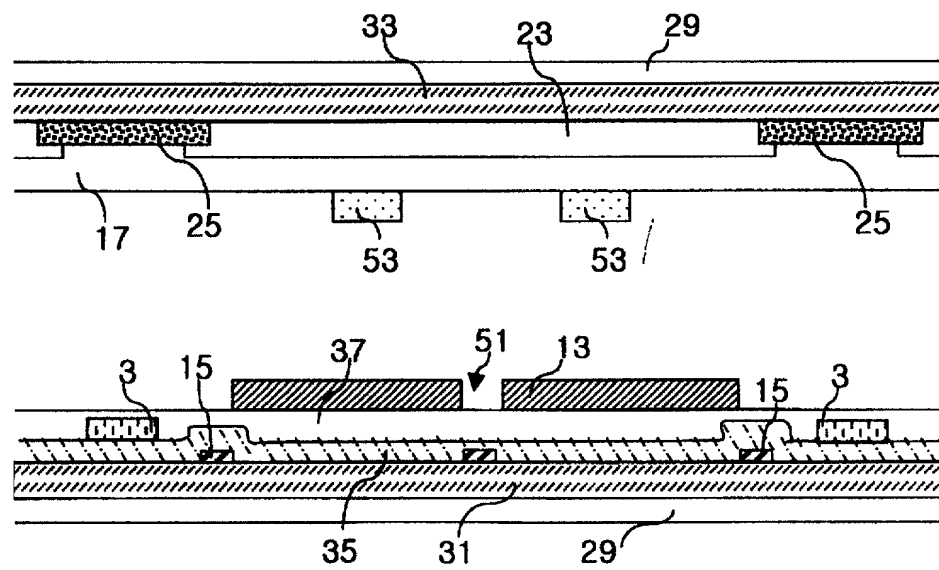


FIG. 5C



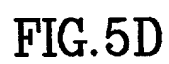


FIG. 6A

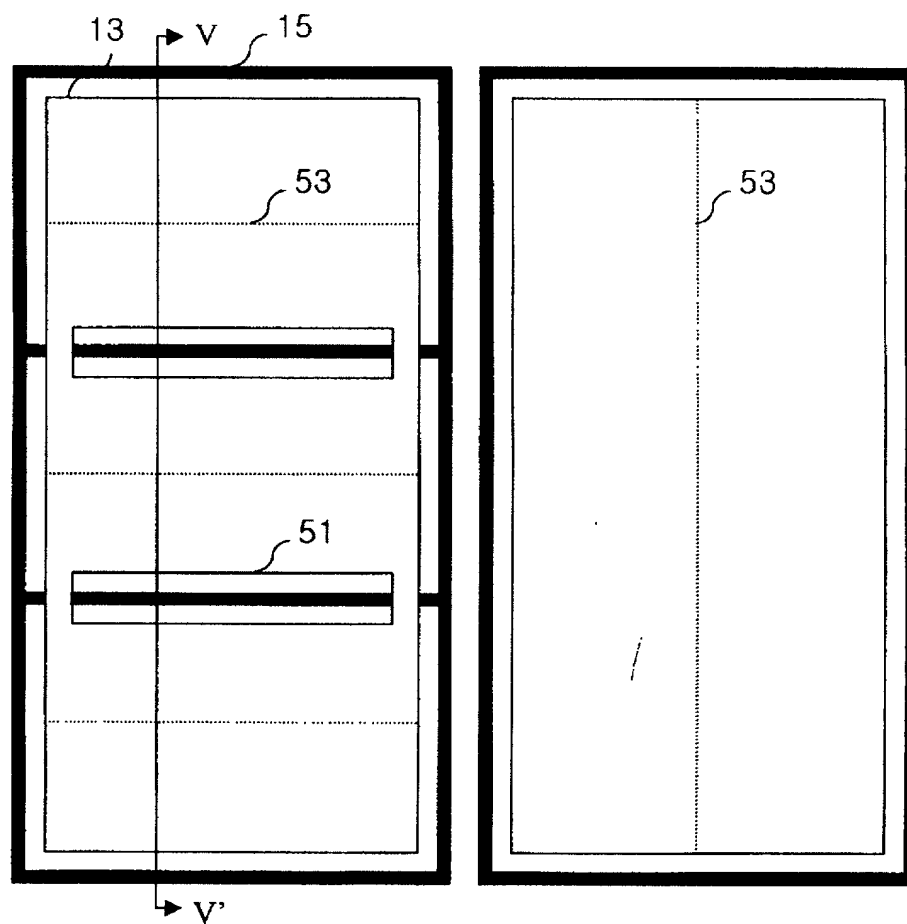


FIG. 6B

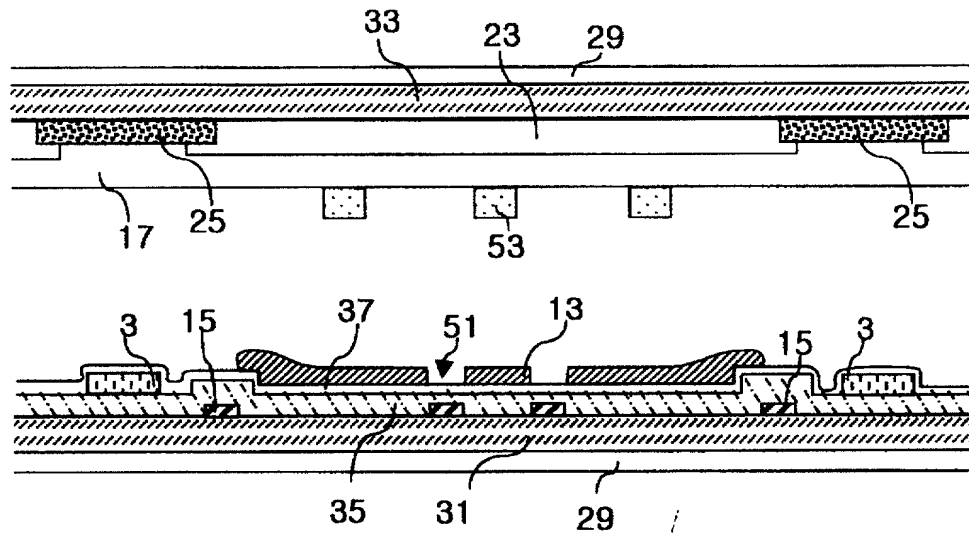


FIG. 6C

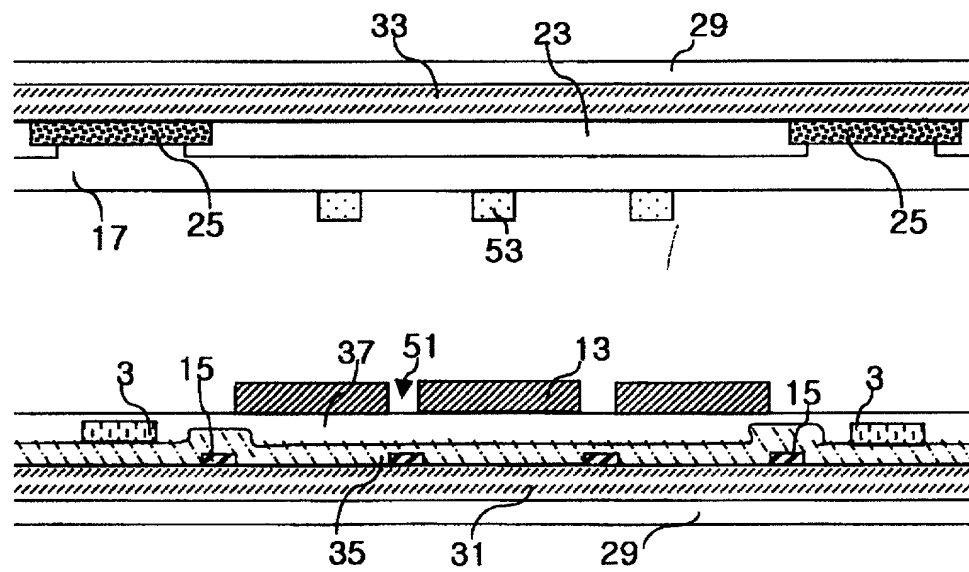


FIG. 6D

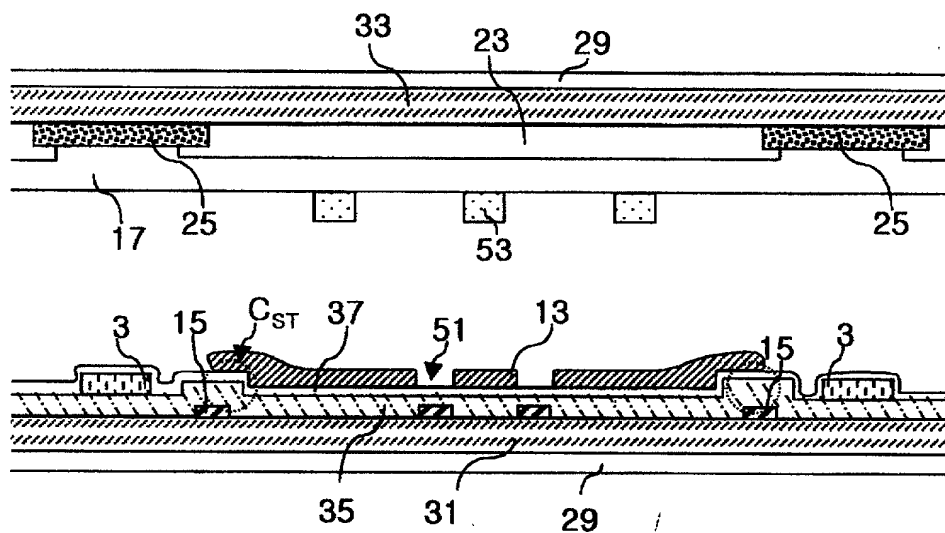


FIG. 6E

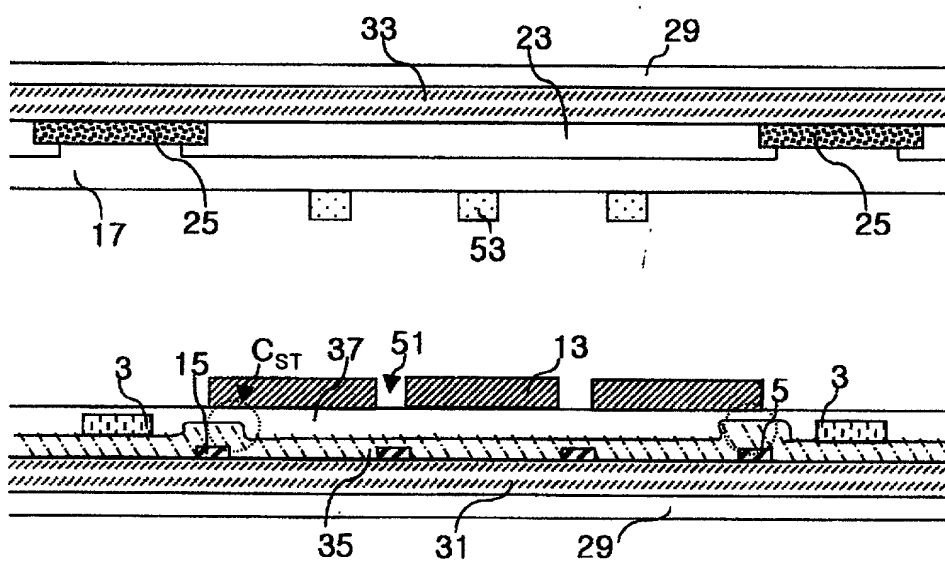


FIG. 7A

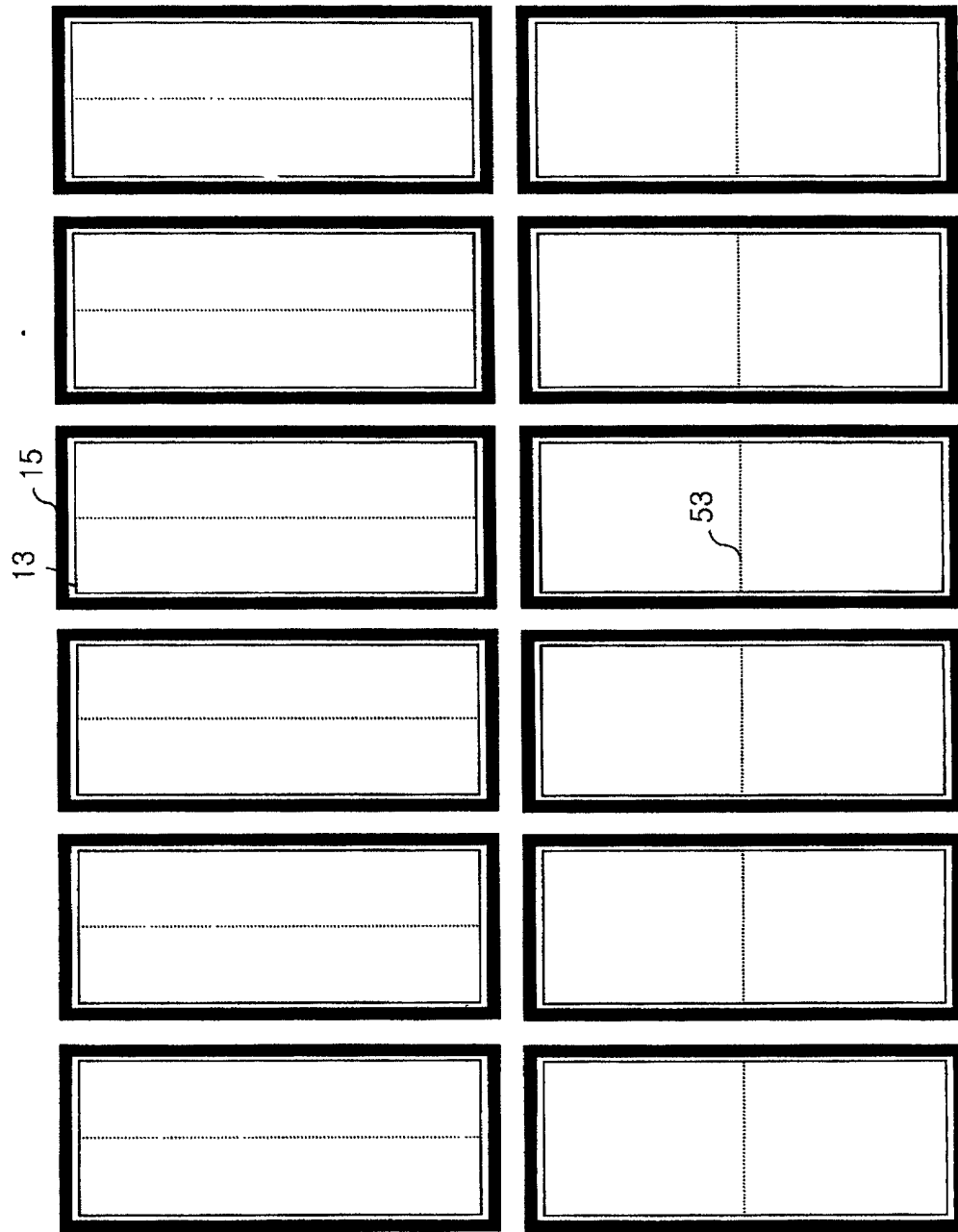


FIG. 7B

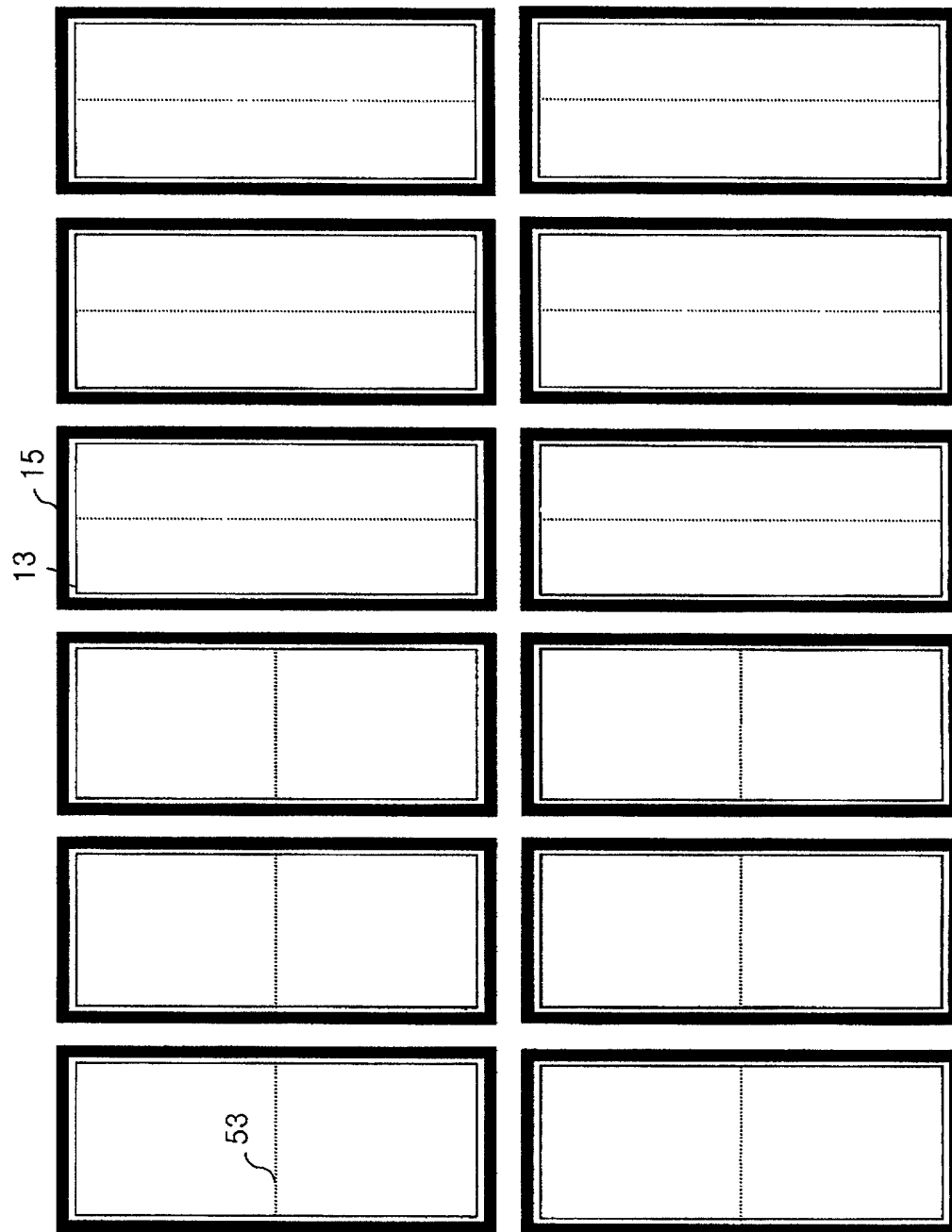


FIG. 7C

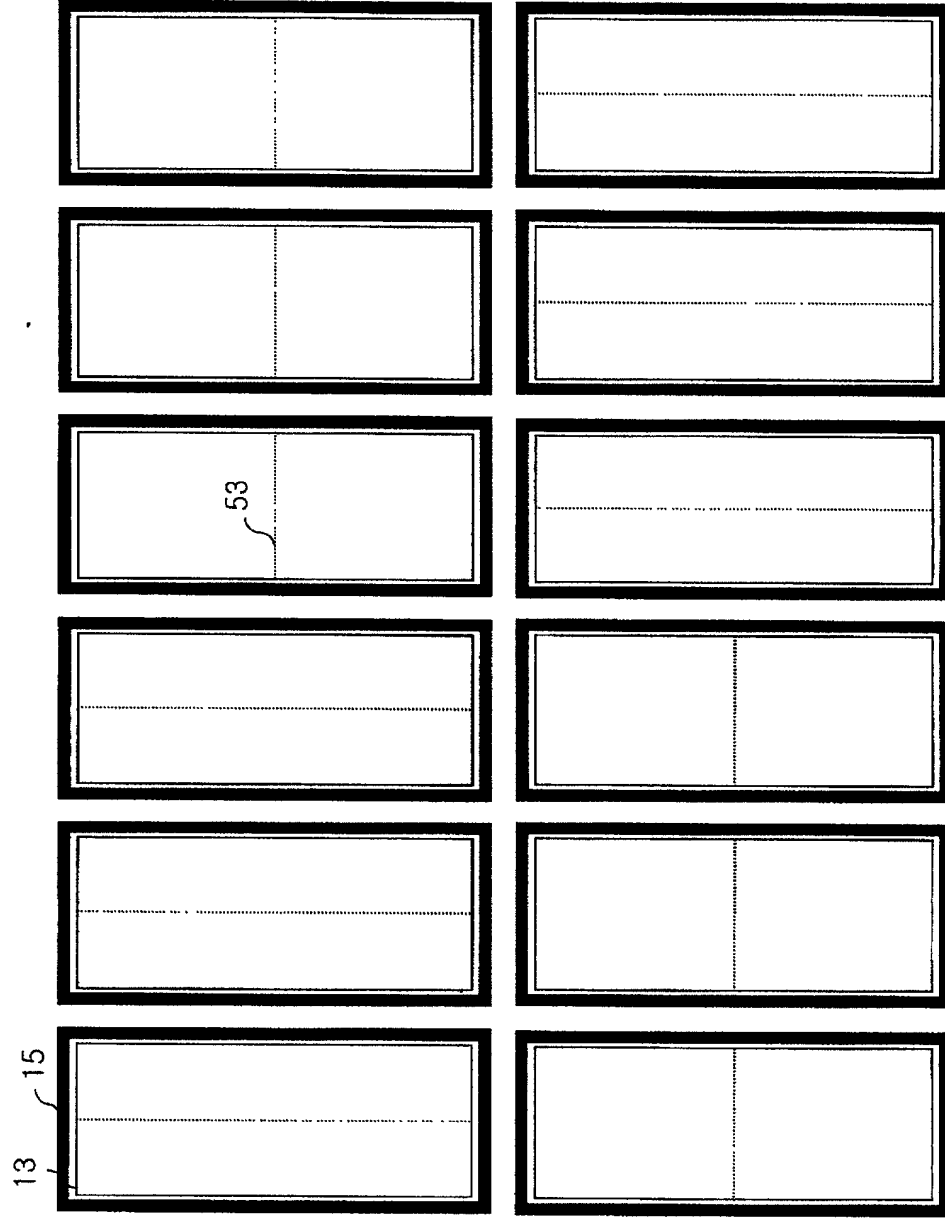


FIG. 7D

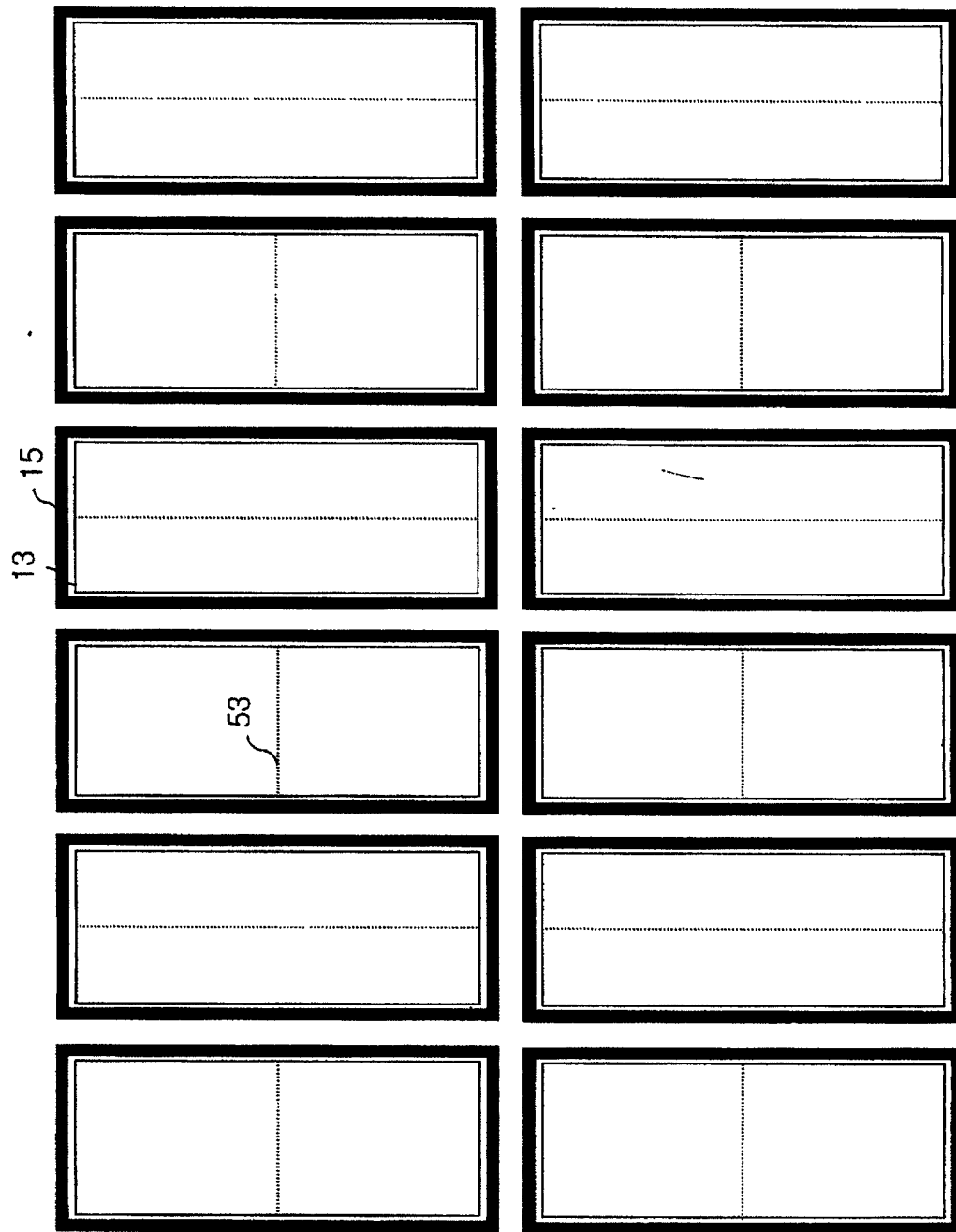


FIG. 7E

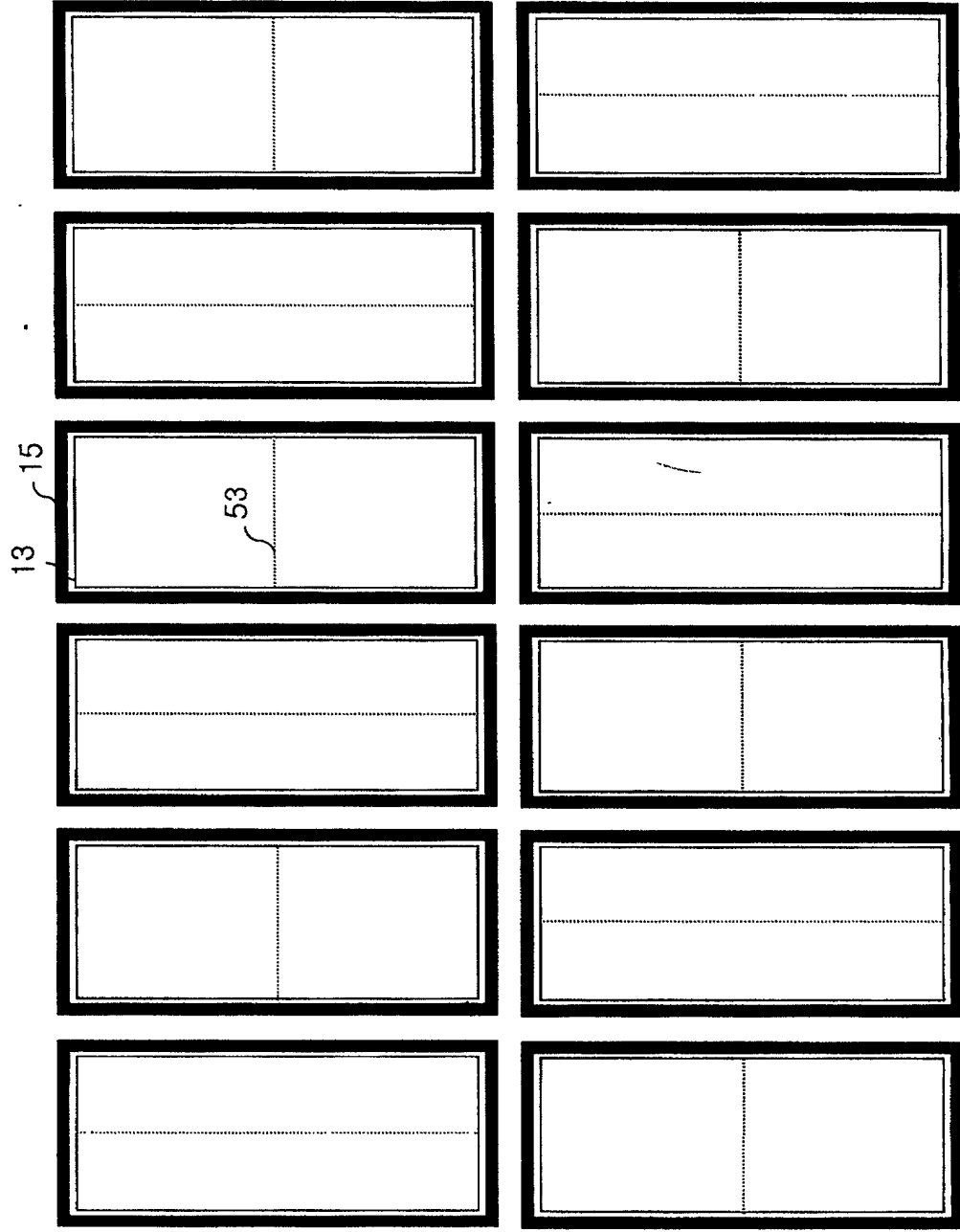


FIG. 8A

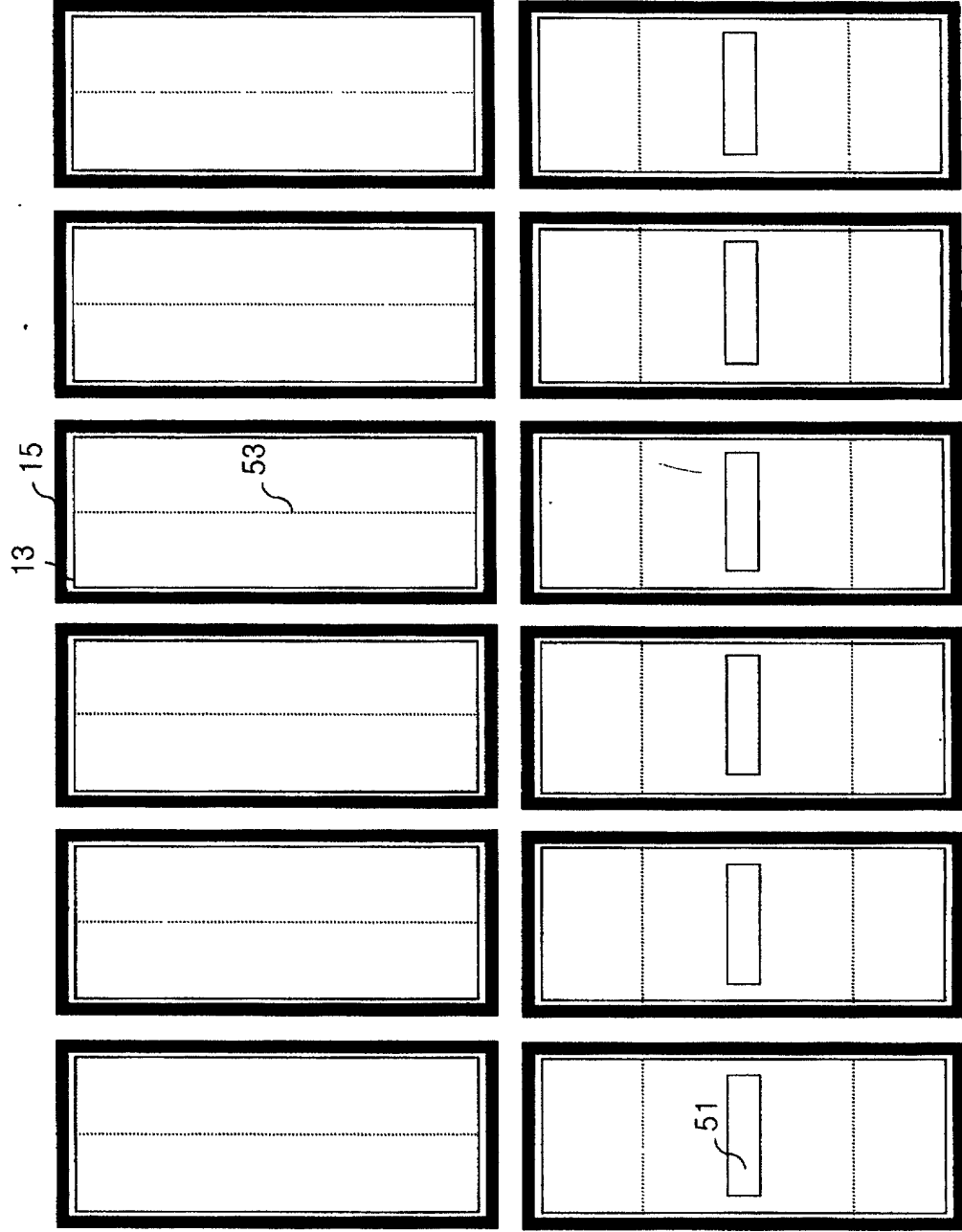


FIG. 8B

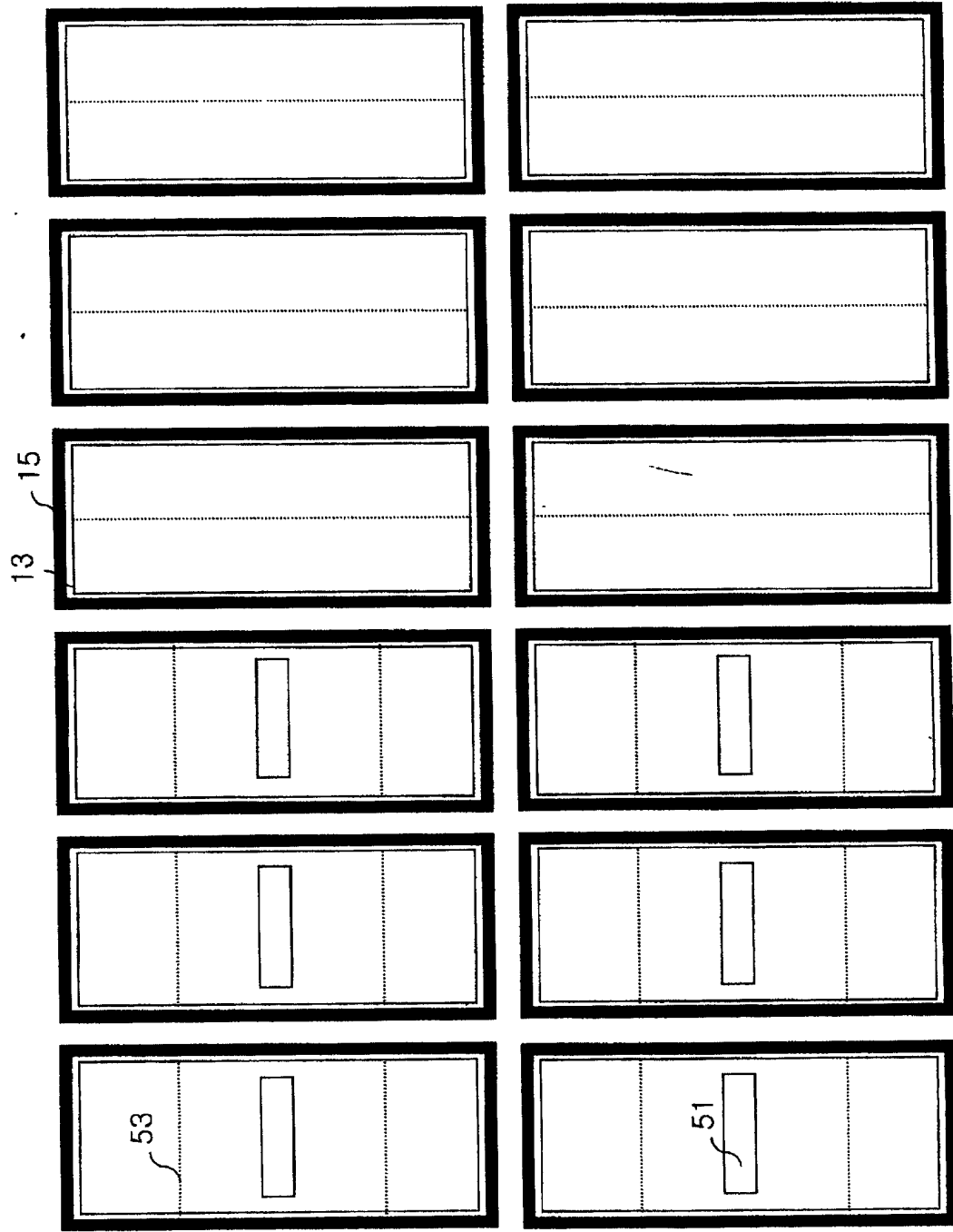


FIG. 8C

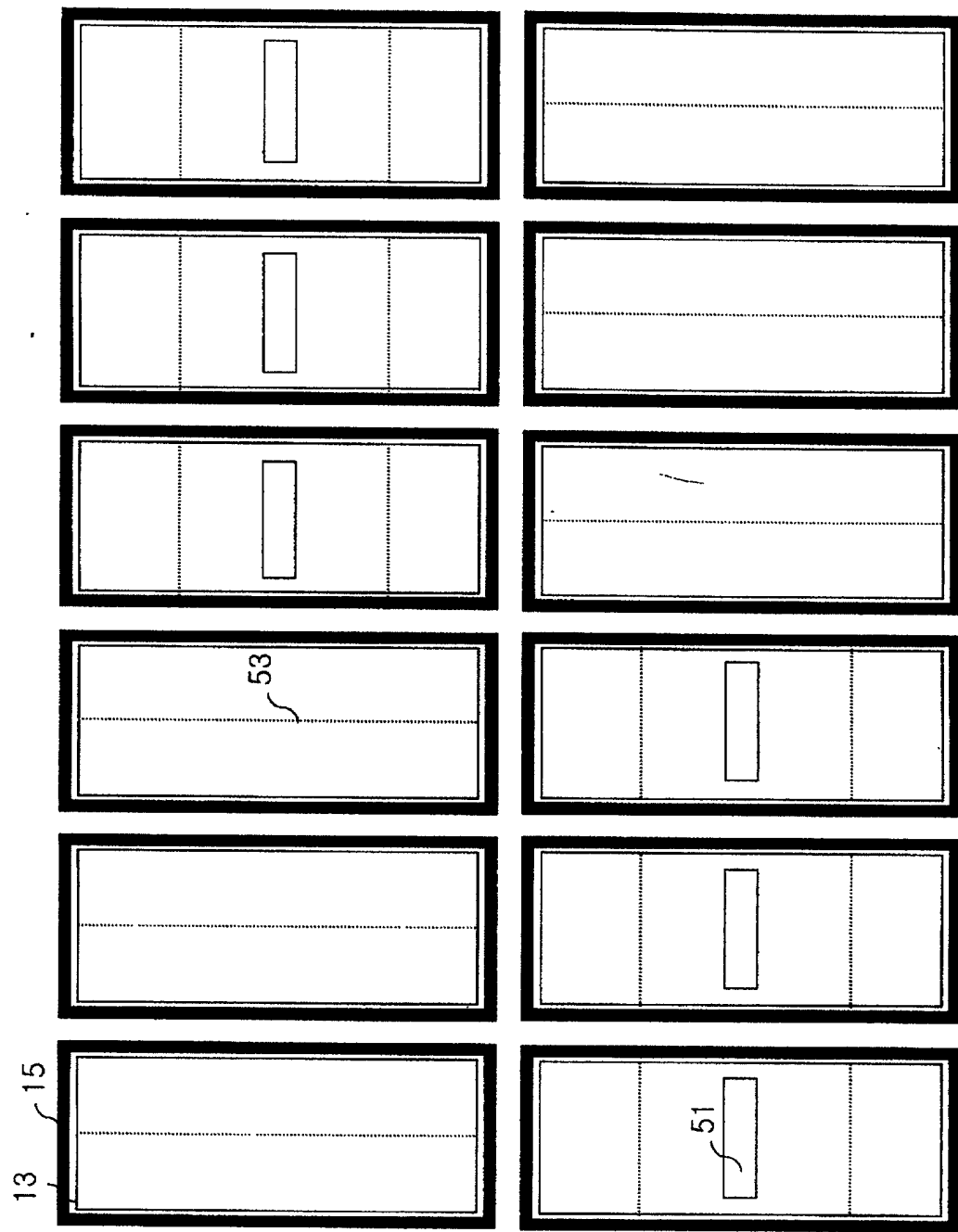


FIG. 8D

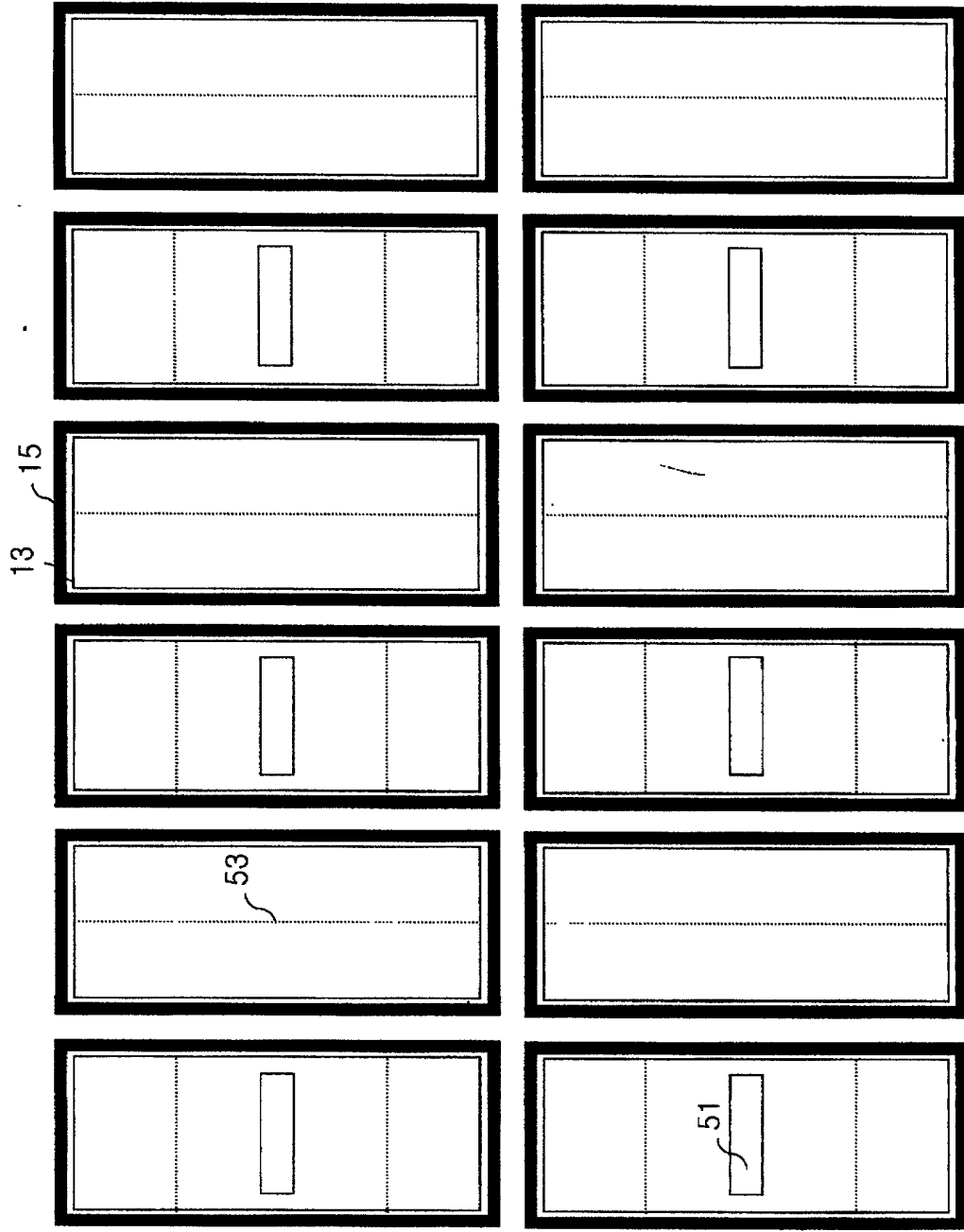


FIG. 9A

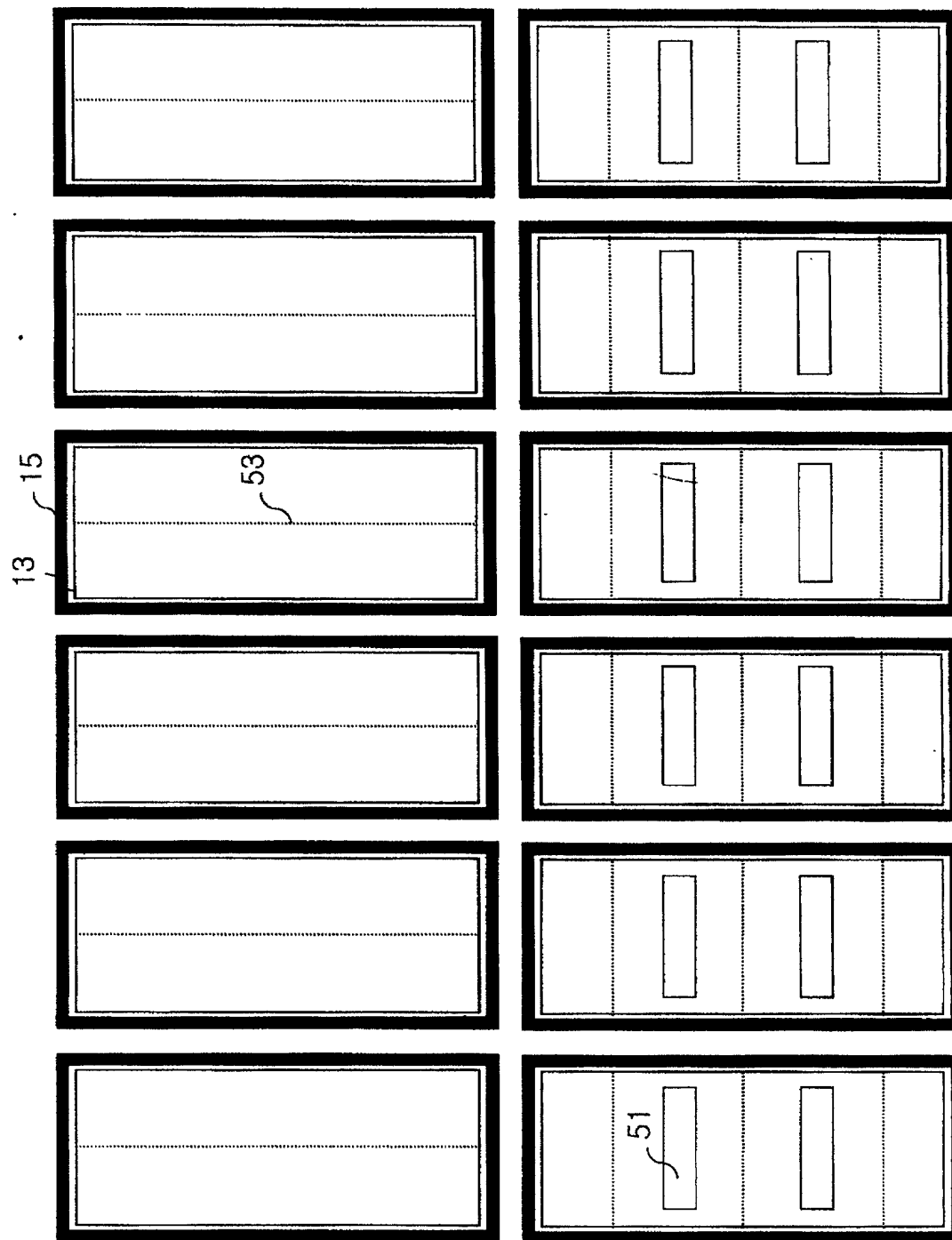


FIG. 9B

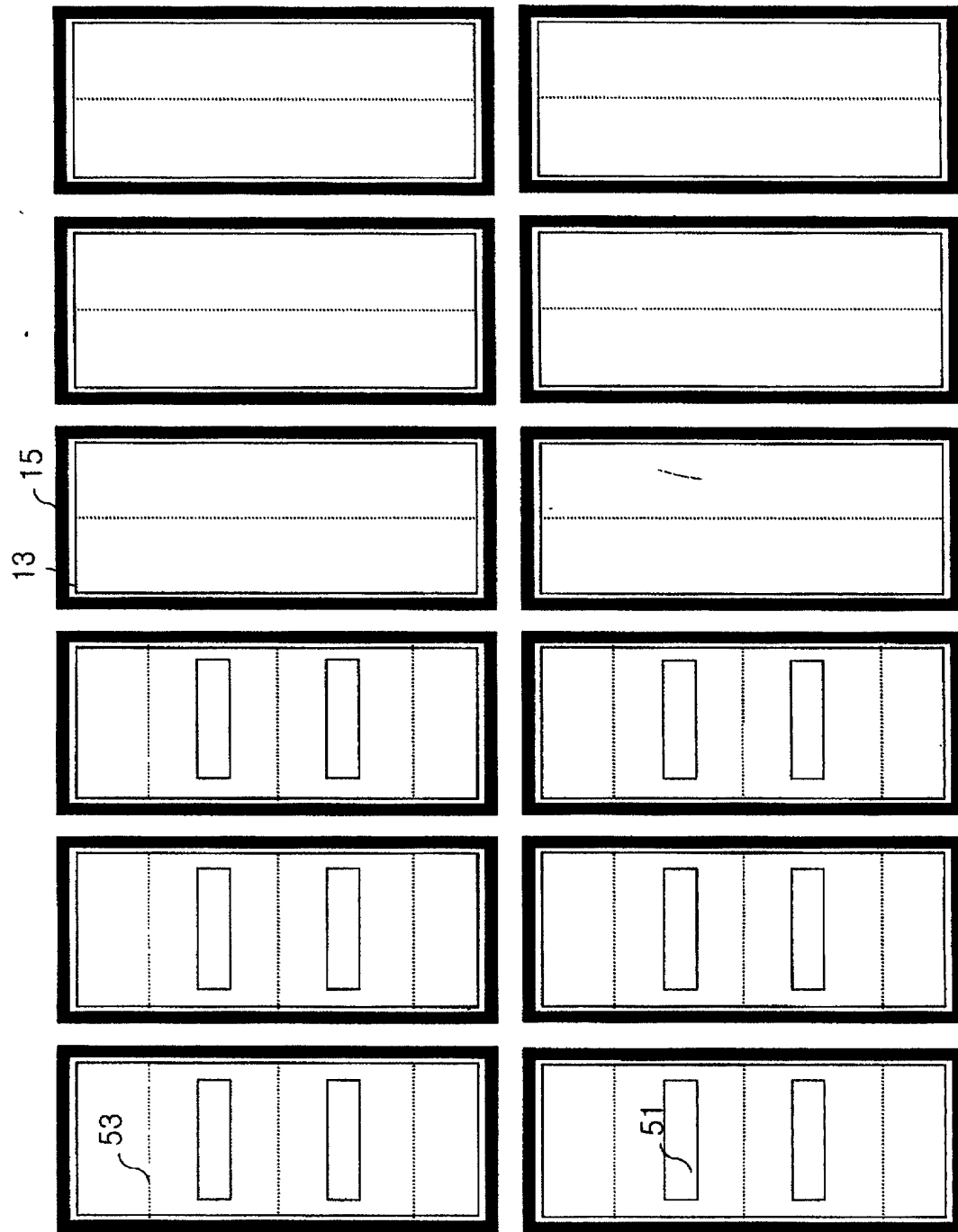


FIG. 9D

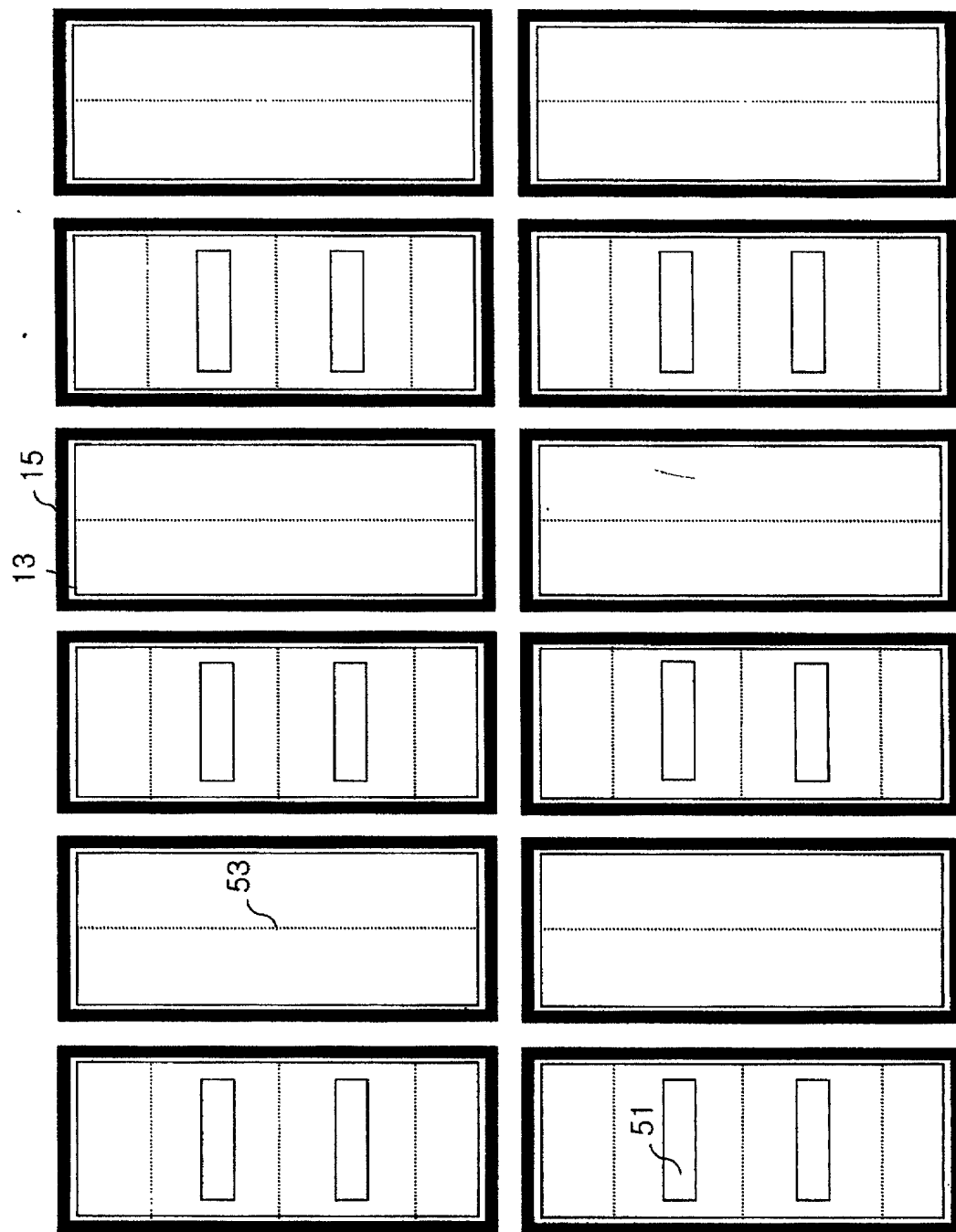


FIG. 9E

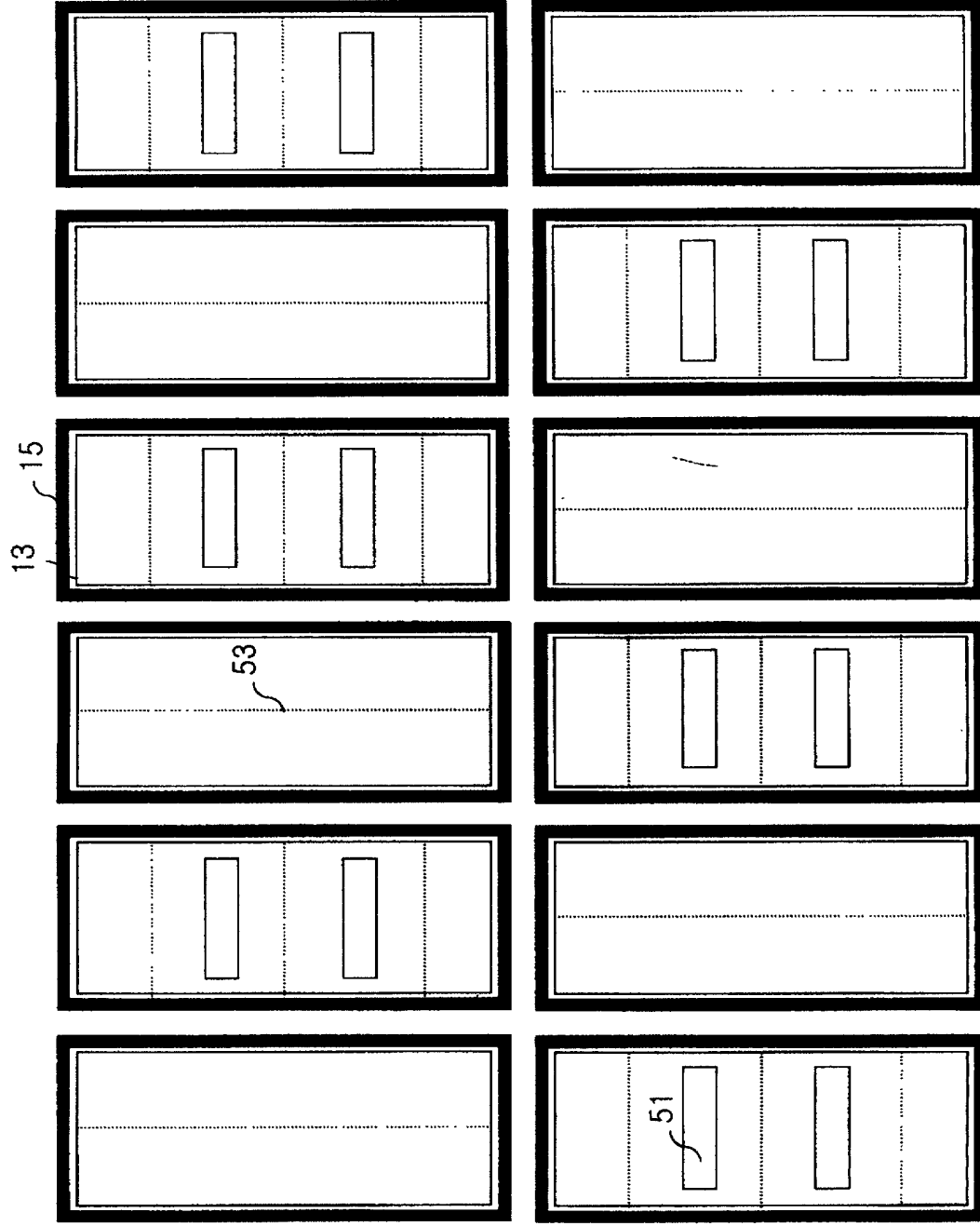


FIG.10A

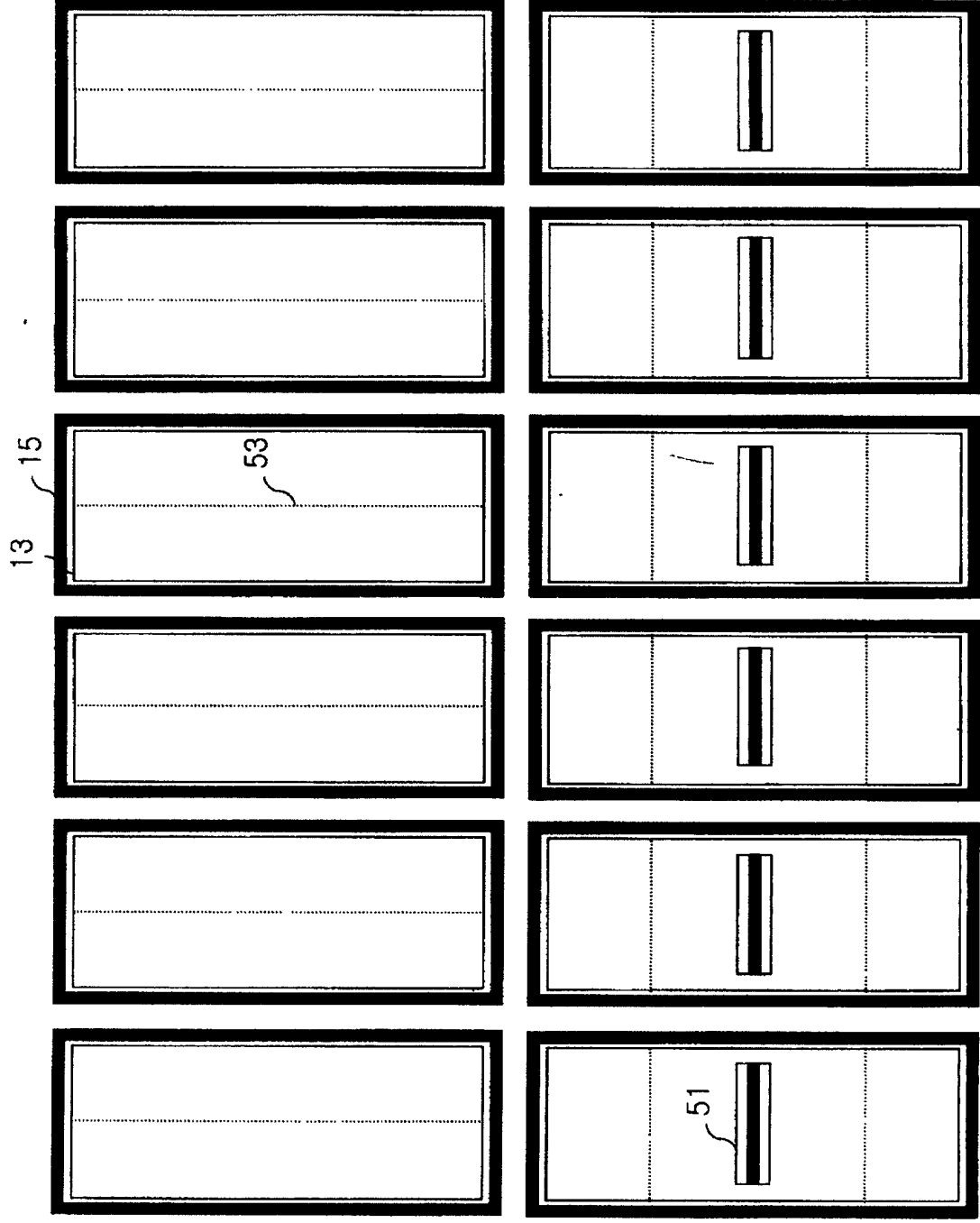


FIG.10B

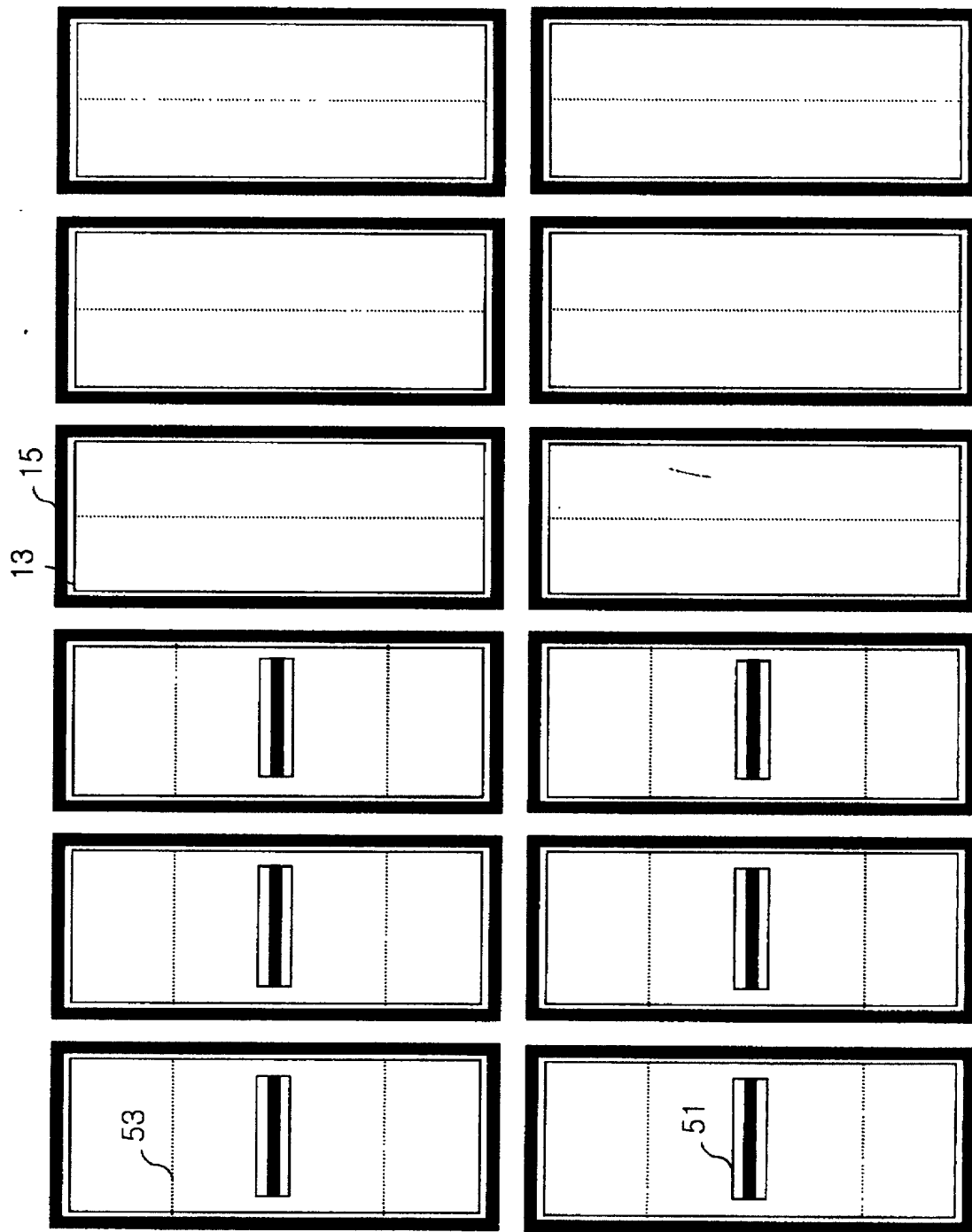


FIG.10C

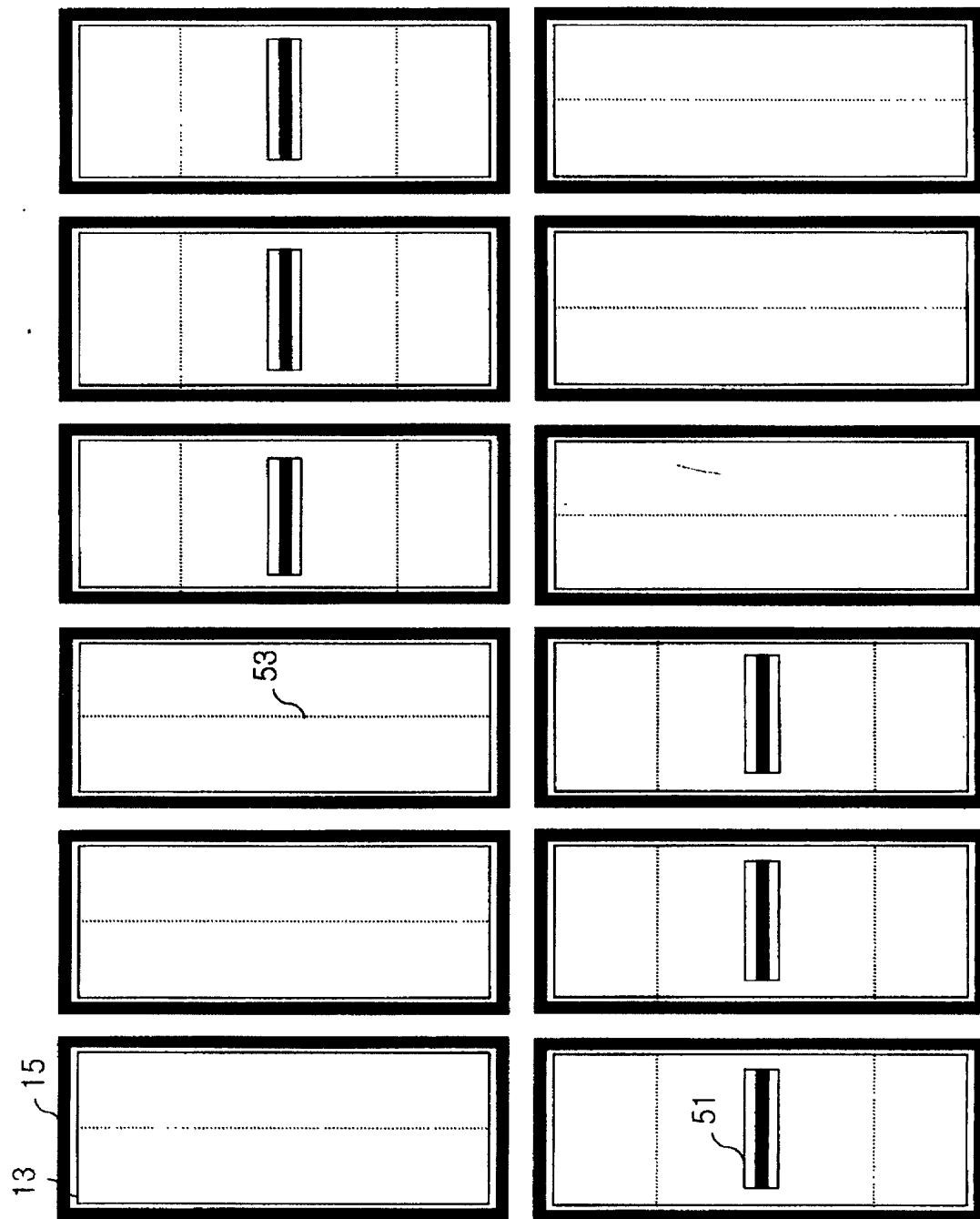


FIG.10D

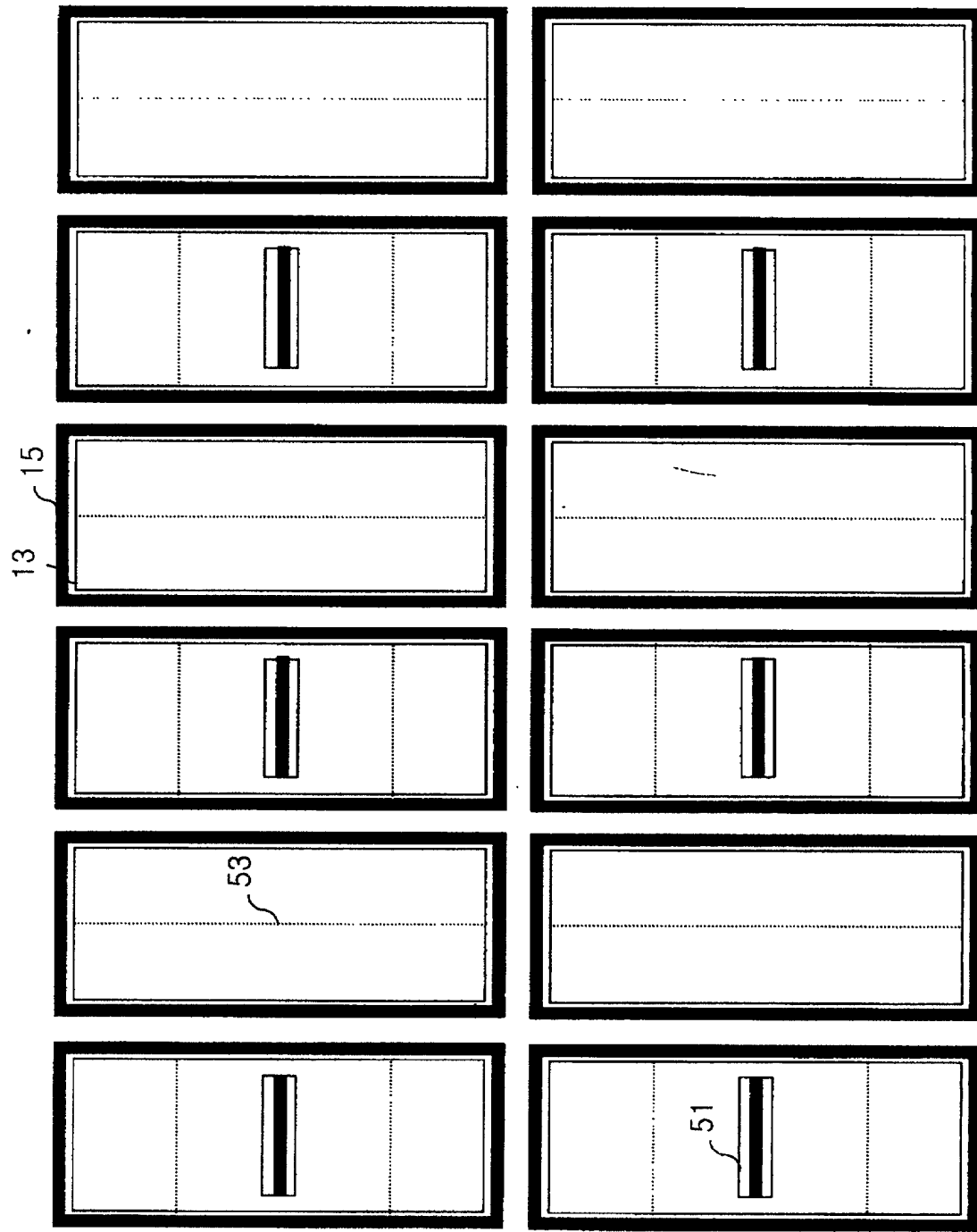


FIG.10E

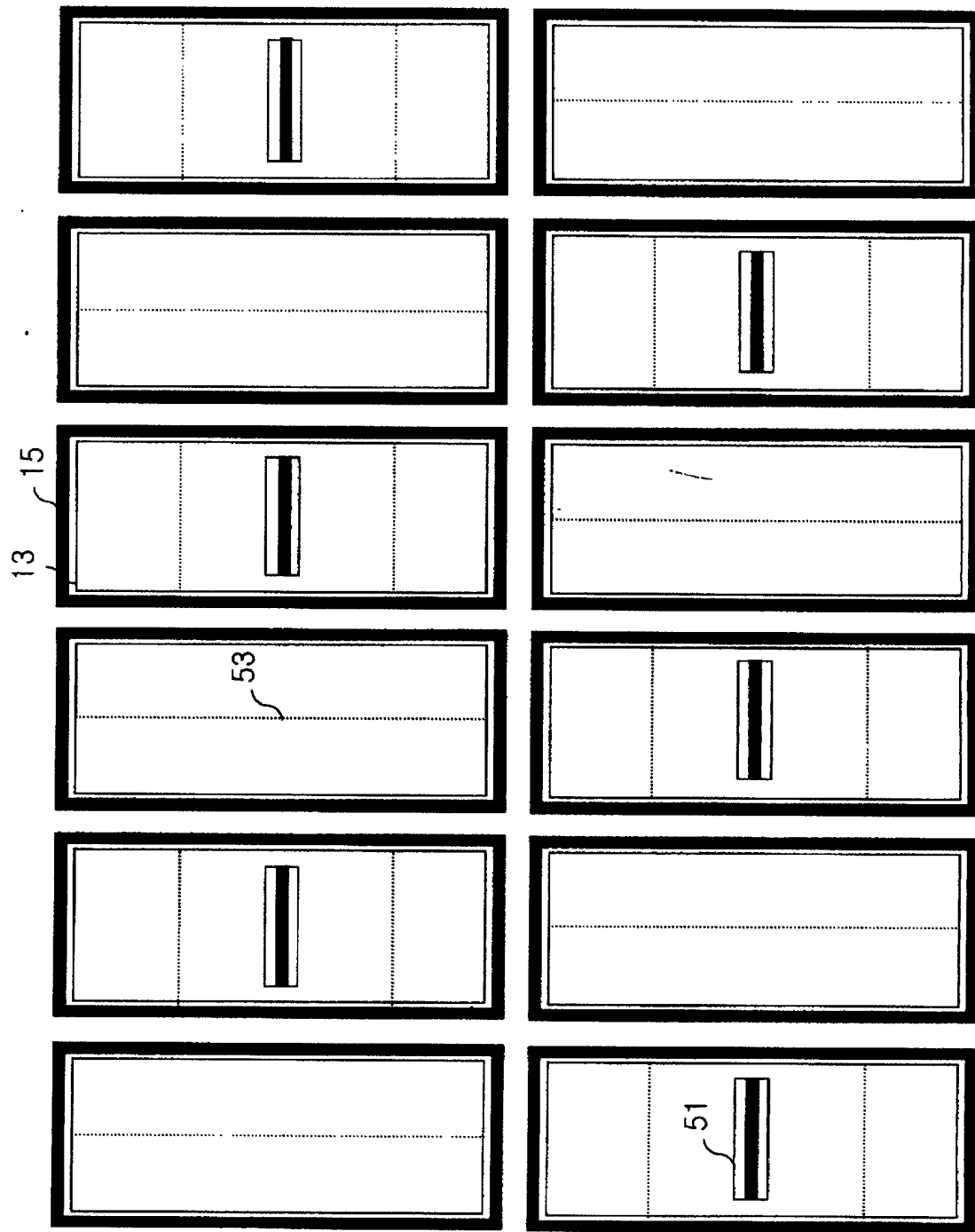


FIG. 11A

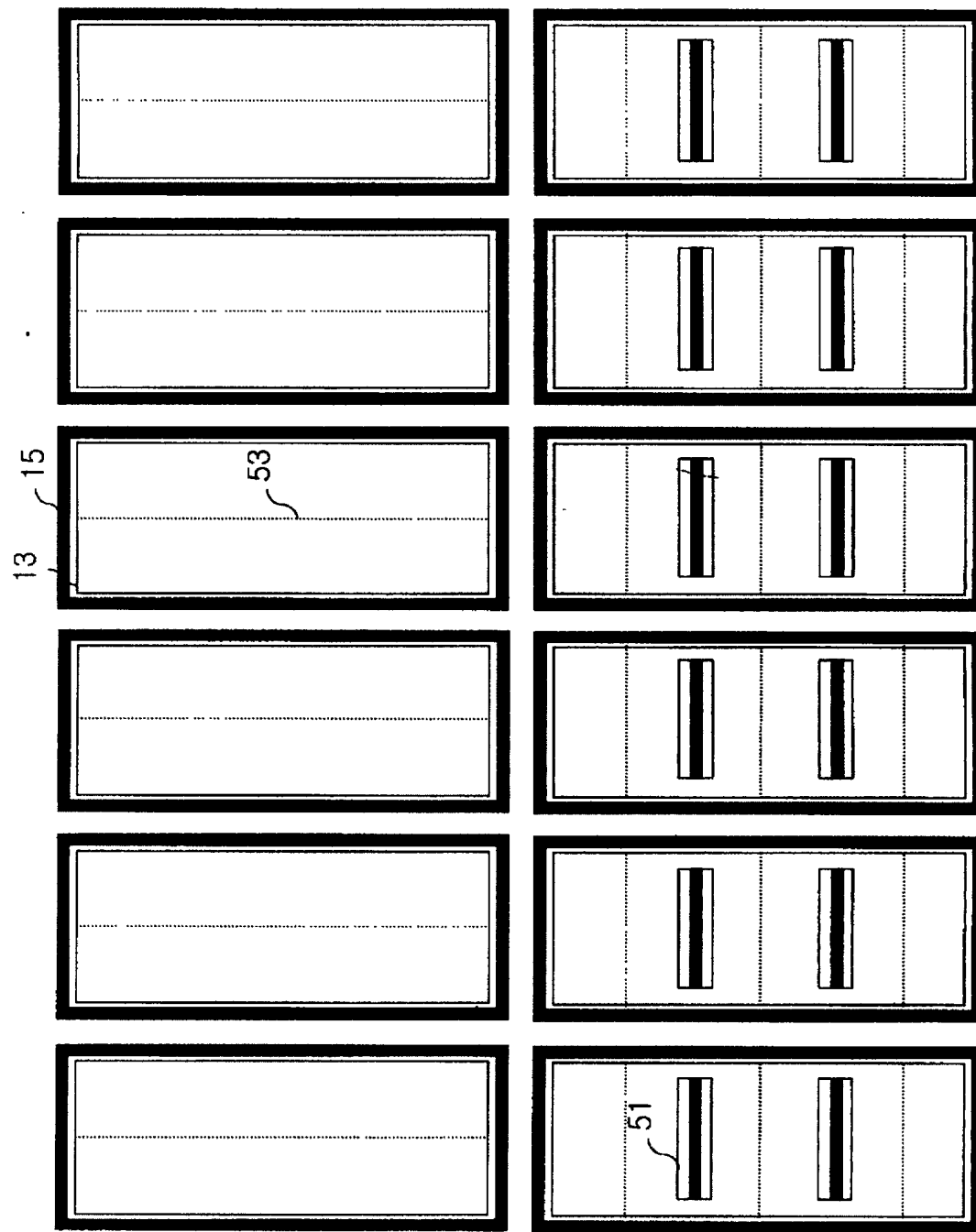


FIG.11B

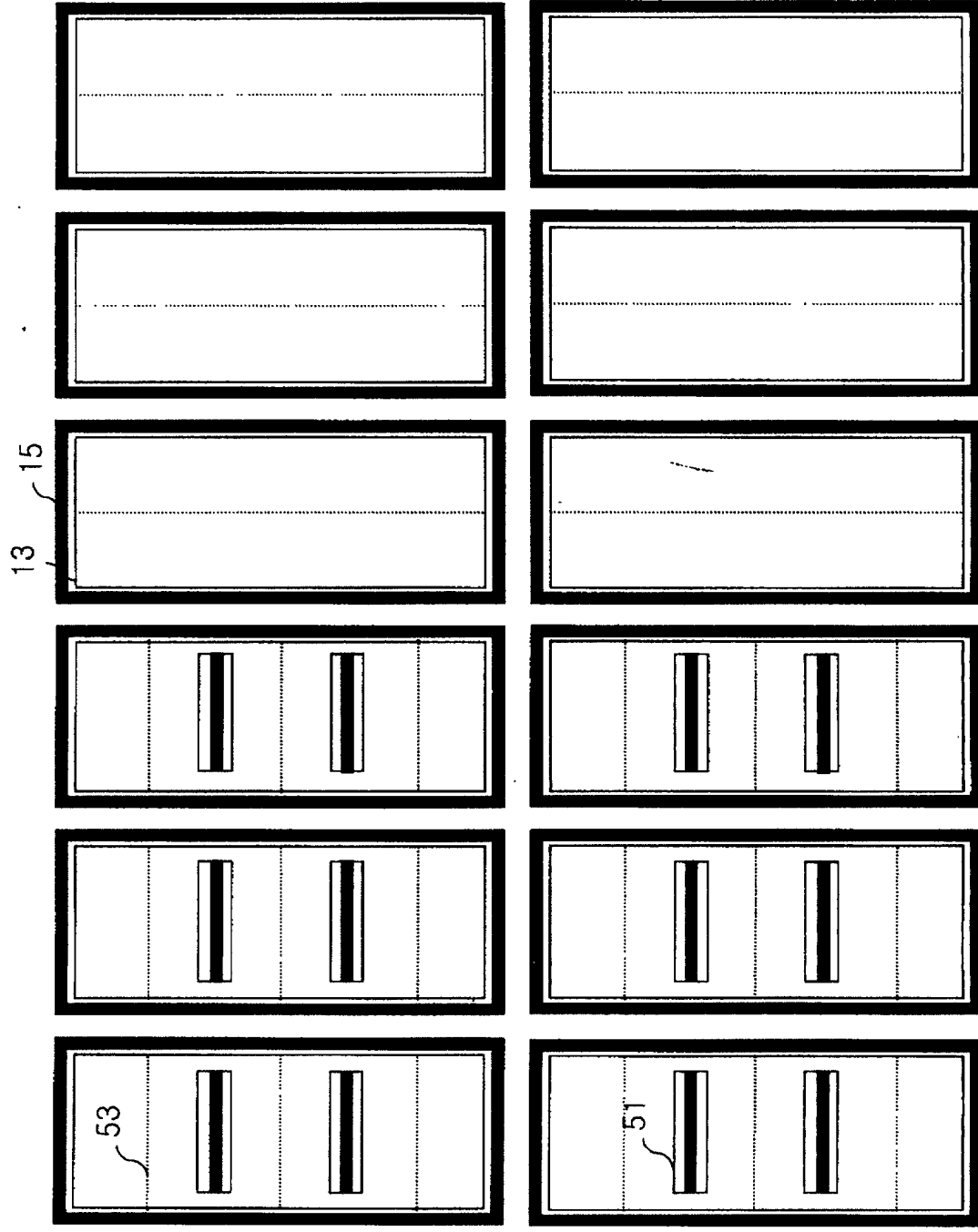


FIG.11D

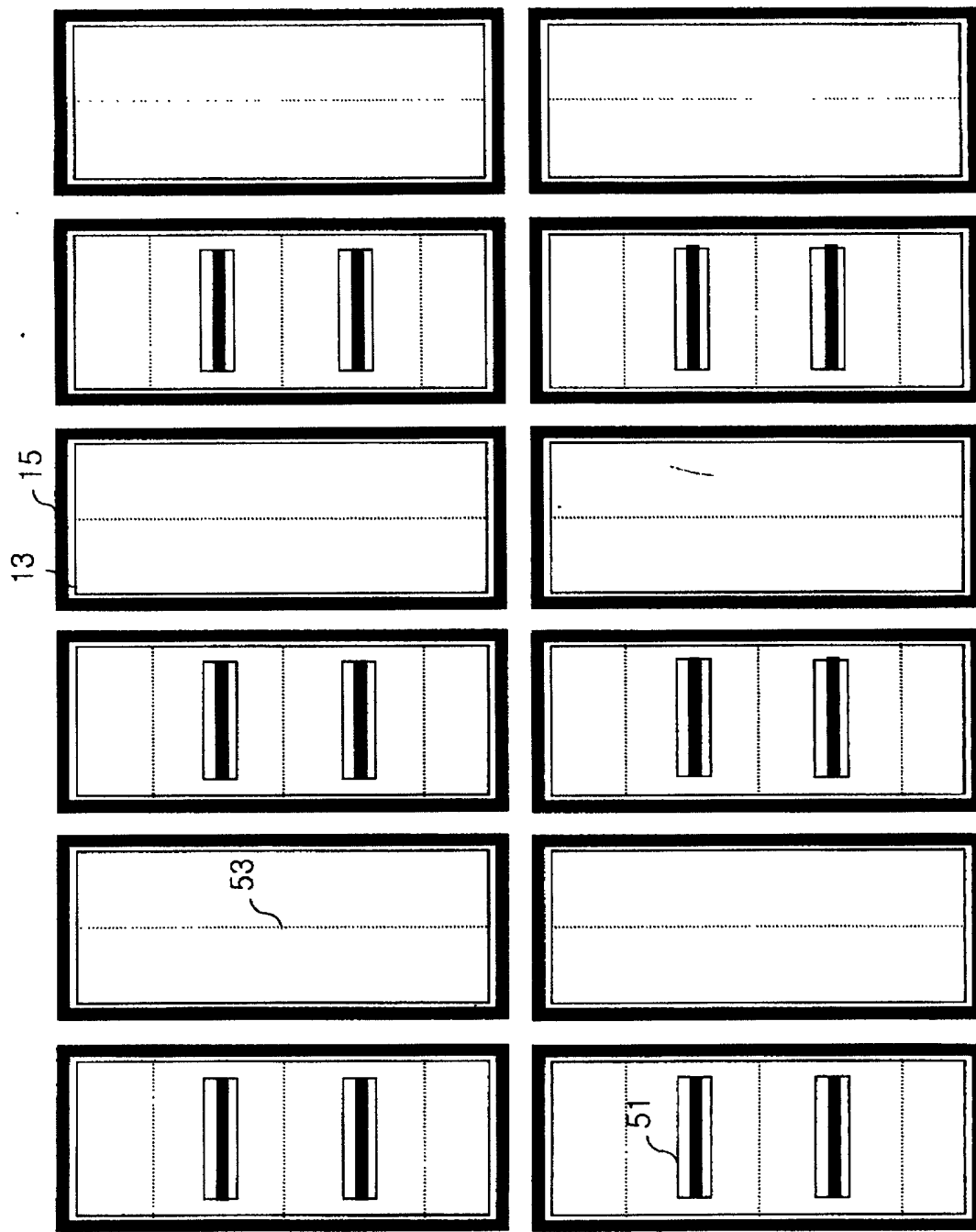
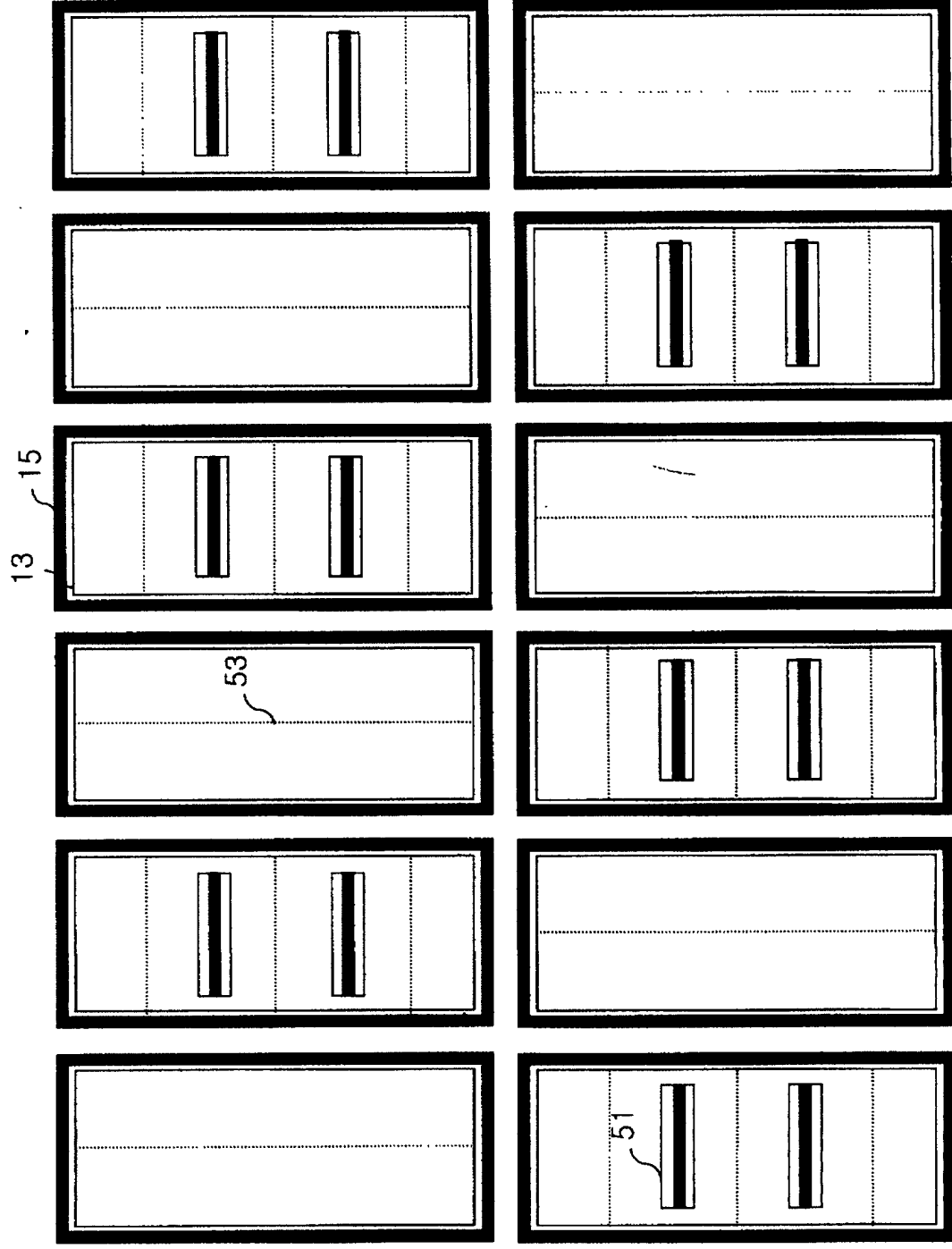


FIG.11E



**DECLARATION
and POWER OF ATTORNEY**

As a below named inventor, I declare that the information given herein is true, that I believe that I am the original, first and sole inventor if only one name is listed at 1 below, or a joint inventor if plural inventors are named below at 1-4, of the invention entitled. MULTI-DOMAIN LIQUID CRYSTAL DISPLAY DEVICE which is described and claimed in: the attached specification; or the specification in Application No filed _____ as amended on _____ (if applicable),

and for which a patent is sought, and that my residence, post office address and citizenship are as stated below next to my name.

I acknowledge my duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

PRIOR FOREIGN APPLICATION(S)			
COUNTRY	APPLICATION NUMBER	DATE OF FILING Month Day Year	PRIORITY CLAIMED UNDER 35 U.S.C. 119
KOREA	1999-9058	March 17, 1999	No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

(Application Number) (Filing Date) (Status)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or Agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

STEVEN B. KELBER, Reg. No. 30,073; MARC R. LABGOLD, Ph.D., Reg. No. 34,651; SONG K. JUNG, Reg. No. 35,210; SHARON E. CRANE, Ph.D., Reg. No. 36,113; CATHERINE RICHARDSON, Reg. No. 39,007; KENNETH D. SPRINGER, Reg. No. 39,843; JAMES M. HEINTZ, Reg. No. 41,828, and AMY L. MILLER, Reg. No. 43,804

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		KO	Doo	Hyun	Kumi-shi	KOREA
3	Name of Inventor	LAST NAME	FIRST NAME	MIDDLE NAME	Residence CITY	STATE OR COUNTRY

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signature of INVENTOR 1	Jang Jin Yoo	DATE:	2000 / 6 / 2
Signature of INVENTOR 2	Doo Hyun Ko	DATE:	2000 / 6 / 8
Signature of INVENTOR 3		DATE:	